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54 **Image encoding and decoding method and apparatus using edge synthesis and inverse wavelet transform.**

57 A digitized image is encoded by detecting edges in the image, encoding the position and sharpness of the detected edges, filtering the image by a low-pass filter to generate a low-frequency image, and encoding the low-frequency image. A digitized image encoded in this way is reconstructed by generating a horizontal edge image and a vertical edge image from the encoded edge position and sharpness information, synthesizing a pair of high-frequency images by filtering the horizontal and vertical edge images with an edge synthesis filter, decoding the low-frequency image, and performing an inverse wavelet transform on the decoded low-frequency image and the high-frequency images.

FIG. 1

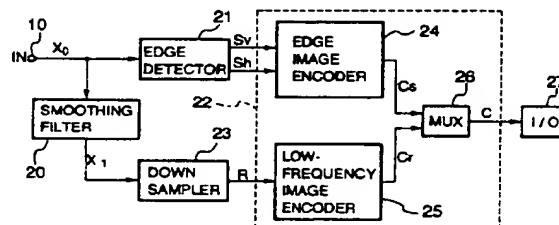
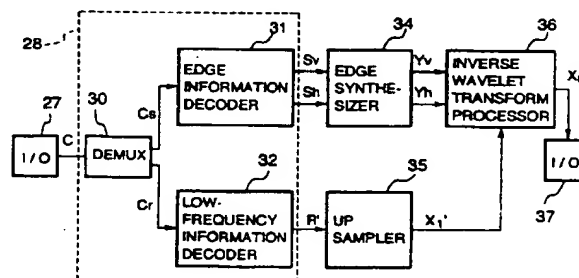


FIG. 2



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## BACKGROUND OF THE INVENTION

This invention relates to a compressive image encoding and decoding method using edge synthesis and the inverse wavelet transform, and to digital image encoding and decoding devices employing this method.

Compression is essential for efficient storage and transmission of digitized images. Compression methods have been described by the Joint Photographic Experts Group (JPEG) for still images, and the Motion Picture Experts Group (MPEG) for moving images. The JPEG method involves a discrete cosine transform (DCT), followed by quantization and variable-length encoding. The MPEG method involves detecting motion vectors. Both methods require substantial computation, the detection of motion vectors being particularly demanding.

Recently there has been much interest in the wavelet transform as a means of obtaining high compression ratios with relatively modest amounts of computation. This transform employs a family of wavelets related by dilation and translation; that is, the family consists of occurrences of the same basic wavelet at different locations and on different scales. If the scales form a progressively doubling sequence, and if the basic wavelet is zero everywhere except in a limited domain, wavelet transforms and inverse wavelet transforms can be carried out with efficient computational algorithms.

A wavelet transform can be described as a filtering process executed at each wavelet scale. A digitized image, for example, is transformed by filtering with the basic wavelet, then with the basic wavelet dilated by a factor of two, then with the basic wavelet dilated by a factor of four, and so on.

One prior-art wavelet encoding scheme employs a complementary pair of wavelets to divide an image into a high-frequency component and a low-frequency component. These components contain information about variations on scales respectively less than and greater than a certain cut-off scale. This process is iterated on the low-frequency component with a doubling of the wavelet scale, obtaining new low-frequency and high-frequency components, then iterated again on the new low-frequency component, and so on. After a certain number of iterations, the components are encoded by an encoding scheme that works from low-toward high-frequency information. This scheme enables accurate image reconstruction, but retains too much high-frequency information to achieve high compression ratios.

Another prior-art wavelet encoding scheme employs a basic wavelet that is the first derivative of a smoothing filter (that is, the first derivative of a low-pass filtering function). This type of wavelet acts as a high-pass filter. High-frequency information is obtained by detecting local peaks (local maxima of absolute values) in the result of the wavelet transform, which correspond to edges in the original image. The size and location of the peak values at a selected scale are encoded, along with a low-frequency image obtained by smoothing at the largest scale of the wavelet transform. Fairly high compression ratios can be obtained in this way.

To reconstruct the original image from the encoded data, this prior-art method employs an algorithm derived from a mathematical procedure involving iterated projections in Hilbert space. Under ideal conditions, the projections converge toward a unique set of data that (i) have the required local peak values and (ii) are within the range of the wavelet transform operator. An inverse wavelet transform is then carried out on the converged data to obtain the original image.

It has yet to be shown, however, that the projections always converge, or that data satisfying conditions (i) and (ii) are unique. In practice, there is difficulty in knowing when to stop iterating. For some images, it seems that data satisfying (i) and (ii) are not unique, and instead of converging, the iteration wanders endlessly through Hilbert space, first approaching the desired image transform, then moving away again.

Still higher compression ratios have been obtained by a fractal compression scheme, but this requires a very large amount of computation in the encoding process.

## SUMMARY OF THE INVENTION

It is accordingly an object of the present invention to encode digitized images efficiently, with a high compression ratio but without requiring extensive computation.

Another object of the invention is to encode moving images efficiently.

Still another object is to reconstruct digitized images from encoded information efficiently and reliably.

Yet another object is to reconstruct digitized moving images efficiently and reliably.

The invented method of encoding a digitized image comprises the steps of:

detecting edges in the digitized image;

encoding the position and sharpness of the detected edges to generate edge information;

filtering the digitized image by a low-pass filter to generate a low-frequency image; and

encoding the low-frequency image to generate low-frequency information.

The invented method of reconstructing a digitized image encoded by the invented encoding method comprises the steps of:

generating a horizontal edge image and a vertical edge image from the encoded edge information;  
 synthesizing a pair of high-frequency images by filtering the horizontal edge image and vertical edge  
 image with an edge synthesis filter;  
 decoding the encoded low-frequency information to obtain a low-frequency image; and  
 performing an inverse wavelet transform on the low-frequency image and the high-frequency images.

## BRIEF DESCRIPTION OF THE DRAWINGS

FIG. 1 is a block diagram of a digital image encoder in a first embodiment of the invention.  
 FIG. 2 is a block diagram of a digital image decoder in the first embodiment.  
 FIG. 3 is a graph illustrating pixel values at an edge.  
 FIG. 4 is a graph illustrating pixel values at another edge.  
 FIG. 5 is a graph illustrating pixels that do not form an edge.  
 FIG. 6 is a block diagram of the edge synthesizer in a second embodiment of the invention.  
 FIG. 7 is a block diagram of the digital image encoder in a third embodiment of the invention.  
 FIG. 8 is a block diagram of the digital image decoder in the third embodiment.  
 FIG. 9 is a block diagram of the digital image encoder in a fourth embodiment.  
 FIG. 10 is a block diagram of the digital image encoder in a fifth embodiment.  
 FIG. 11 is a block diagram of the digital image decoder in the fifth embodiment.  
 FIG. 12 is a block diagram of the digital image encoder in a sixth embodiment.  
 FIG. 13 is a block diagram of the digital image encoder in a seventh embodiment, for encoding moving  
 images.  
 FIG. 14 is a block diagram of the digital image decoder in the seventh embodiment.  
 FIG. 15 is a block diagram of the digital image encoder in an eighth embodiment.  
 FIG. 16 is a block diagram of the digital image encoder in a ninth embodiment.  
 FIG. 17 is a block diagram of the digital image encoder in a tenth embodiment.  
 FIG. 18 is a block diagram of the digital image decoder in the tenth embodiment.  
 FIG. 19 is a block diagram of the digital image encoder in an eleventh embodiment.  
 FIG. 20 is a block diagram of the digital image encoder in a twelfth embodiment.

## DETAILED DESCRIPTION OF THE INVENTION

Embodiments of the invention will be now described with reference to the attached illustrative drawings,  
 starting with the simplest case in which the inverse wavelet transform is limited to a single scale. The following  
 terminology will be used.

A (two-dimensional) digitized image is an array of pixels having values  $x(i, j)$ , where the horizontal coordinate  $i$  and vertical coordinate  $j$  range over sets of integers. The range of these coordinates is the size of the image, i.e. the number of pixels in the horizontal and vertical directions. The pixel values represent, for example, intensity gradations.

A one-dimensional image is a horizontal or vertical line in a two-dimensional digitized image, i.e. a set of pixels with values  $x(i)$  indexed by a single coordinate.

A standard edge is a one-dimensional image having just one sharp variation point. An example would be an image with pixel values that change first at one constant rate, then at a different constant rate, such as the following:

... 2.0    1.5    1.0    0.5    0    0.5    1.0    1.5    2.0 ...

In this standard edge the pixel values first decrease at a rate of -0.5 per pixel, then increase at a rate of +0.5 per pixel. Other standard edges of the same general type are shown in FIGs. 3 and 4. A standard edge serves as a basic model for all the edges occurring in a digitized image.

Down-sampling a digitized image means reducing its size by a process generally analogous to photographic reduction. Up-sampling means increasing the size of an image by a process generally analogous to photographic enlargement. In up- or down-sampling the entire image is enlarged or reduced, its features increasing or decreasing in scale.

A (one-dimensional) filter is a set of coefficients  $f(k)$ , where  $k$  ranges over the integers. The non-zero coefficients  $f(k)$  are the taps of the filter.

The Fourier transform  $F$  of a filter  $f$  is defined as follows, where summation is over all taps,  $e$  is the natural logarithm base, and  $j$  is the square root of minus one.

$$F(\omega) = \sum_k f(k) \cdot e^{-j\omega k}$$

5 The conjugate  $f^*$  of a filter  $f$  is obtained by reversing the order of coefficients:

$$f^*(k) = f(-k)$$

The Fourier transform of  $f^*$  is therefore the complex conjugate of the Fourier transform of  $f$ .

A filter has even symmetry around  $i_0$  if it has equal values on both sides of  $i_0$ ; that is, for all integers  $k$ :

$$f(i_0 - k) = f(i_0 + k)$$

10 In particular, a filter has even symmetry around zero if it is equal to its own conjugate.

A filter has odd symmetry around  $i_0$  if it has opposite values on both sides of  $i_0$ , that is:

$$f(i_0 - k) = -f(i_0 + k)$$

A filter satisfies the exact reconstruction condition if its Fourier transform  $F$  satisfies the following condition for all values of  $\omega$ :

$$15 \quad |F(\omega)|^2 + |F(\omega + \pi)|^2 = 1$$

Two filters with Fourier transforms  $G$  and  $H$  satisfy the exact reconstruction condition if:

$$|G(\omega)|^2 + |H(\omega)|^2 = 1$$

Filters satisfying these exact reconstruction conditions, also referred to as perfect reconstruction conditions, are well known in the wavelet transform art. A pair of filters satisfying the second exact reconstruction condition above is referred to as a complementary pair.

20 To filter a one-dimensional image  $x$  by a filter  $f$  means to obtain a new one-dimensional image  $y$  with the following pixel intensities:

$$25 \quad y(i) = \sum_k x(i + k) \cdot f(k)$$

This operation is equivalent to the conventional convolution operation using the conjugate filter  $f^*$ .

$$30 \quad y(i) = \sum_k x(i - k) \cdot f^*(k)$$

The difference between convolution and filtering, as the term is used herein, is purely one of notation. The meaning of the following description and claims would be unchanged if the terms convolution and convolve were used throughout.

To filter a digitized image  $x$  horizontally by a filter  $f$  means to obtain a new image  $y$  as follows:

$$40 \quad y(i, j) = \sum_k x(i + k, j) \cdot f(k)$$

Similarly, filtering image  $x$  vertically by  $f$  means:

$$45 \quad y(i, j) = \sum_k x(i, j + k) \cdot f(k)$$

50 Filtering can be done two-dimensionally by, for example, filtering first in the horizontal direction, then in the vertical direction; or vice versa.

A high-pass filter is a filter that retains small-scale variations and rejects large-scale variations. A low-pass filter rejects small-scale variations and retains large-scale variations. Smoothing filter is a synonym for low-pass filter.

55 The cut-off frequency of a high-pass or low-pass filter indicates the scale that divides the retained variations from the rejected variations. A lower cut-off frequency corresponds to a larger scale of variations. Frequency actually refers to the variable  $\omega$  in the Fourier transform  $F(\omega)$  of the filter.

Generally speaking, the Fourier transform of a high-pass filter satisfies  $F(0) = 0$ , while for a low-pass filter,  $|F(0)| > 0$ . If  $|F(0)| = 1$ , a low-pass filter can retain large-scale variations without changing their range of

variation. Incidentally,  $F(0)$  is the sum of the filter coefficients  $f(k)$ .

#### First embodiment

Referring to FIG. 1, the digital image encoder in the first embodiment comprises an input terminal 10, smoothing filter 20, edge detector 21, encoding section 22, and down sampler 23. The encoding section 22 comprises an edge image encoder 24, low-frequency image encoder 25, and multiplexer 26.

A digitized image  $X_0$  is input from the input terminal 10 to both the smoothing filter 20 and edge detector 21. The smoothing filter 20 outputs a low-frequency image  $X_1$  to the down sampler 23, which outputs a reduced image  $R$  to the low-frequency image encoder 25 in the encoding section 22. The low-frequency image encoder 25 encodes the reduced image  $R$  to obtain low-frequency information  $Cr$ . The edge detector 21 detects edges in the input image  $X_0$  and outputs horizontal and vertical edge images  $Sh$  and  $Sv$  to the edge image encoder 24 in the encoding section 22. The edge image encoder 24 encodes these edge images to obtain edge information  $Cs$ . The multiplexer 26 combines the low-frequency information  $Cr$  and edge information  $Cs$  into an encoded image  $C$ , which is output to a first input/output device 27 such as a communication channel, semiconductor memory, or magnetic disk storage device.

Referring to FIG. 2, the digital image decoder has a decoding section 28 comprising a demultiplexer 30, edge information decoder 31, and low-frequency information decoder 32. The digital image decoder also has an edge synthesizer 34, up sampler 35, and inverse wavelet transform processor 36.

The demultiplexer 30 receives the encoded image  $C$  from the first input/output device 27 and separates it into edge information  $Cs$ , which it supplies to the edge information decoder 31, and low-frequency information  $Cr$ , which it supplies to the low-frequency information decoder 32. The edge information decoder 31 decodes  $Cs$  to obtain the edge images  $Sh$  and  $Sv$ , from which the edge synthesizer 34 synthesizes horizontal and vertical high-frequency images  $Yh$  and  $Yv$ . The low-frequency information decoder 32 decodes  $Cr$  to obtain a decoded reduced image  $R'$ , which the up sampler 35 enlarges to obtain a decoded low-frequency image  $X_1'$ . The inverse wavelet transform processor 36 receives the high-frequency images  $Yh$  and  $Yv$  from the edge synthesizer 34 and the low-frequency image  $X_1'$  from the up sampler 35, carries out an inverse wavelet transform, and outputs a reconstructed image  $X_0'$  to an input/output device 37 such as a video display unit or printer.

Each of the elements in the FIGS. 1 and 2 comprises a memory for storing data and well-known circuits for performing arithmetic and logic operations. Descriptions of the circuit configurations will be omitted to avoid obscuring the invention with irrelevant detail. The entire digital image encoder can be fabricated as a single semiconductor integrated circuit, or can be built from a small number of such circuits; likewise the digital image decoder. The invention can also be practiced by providing a general-purpose processor such as a microprocessor or digital signal processor with memory for storing data and programs for executing the functions of the individual elements in FIG. 1 or FIG. 2.

Next the operation will be described, starting with the operation of the encoder in FIG. 1.

The input image  $X_0$  is a digitized image with pixel values  $X_0(i, j)$ . The smoothing filter 20 is a low-pass filter with even symmetry around zero and an odd number of taps. The tap coefficients will be denoted  $h_0(k)$ , where  $k$  ranges from  $-N$  to  $N$ , so the filter has  $2N + 1$  taps and the even symmetry condition is:

$$h_0(-k) = h_0(k) \quad (k = 1, 2, \dots, N)$$

The low-pass filter  $h_0$  satisfies the exact reconstruction condition, and the sum of its tap coefficients is plus or minus one. That is, its Fourier transform  $H_0$  satisfies the following conditions:

$$|H_0(\omega)|^2 + |H_0(\omega + \pi)|^2 = 1$$

$$|H_0(0)| = 1$$

The smoothing filter 20 performs a two-dimensional low-pass filtering operation by filtering the input image  $X_0$  with  $h_0$  horizontally and vertically, obtaining:

$$X_1(i, j) = \sum_{k=-N}^N \sum_{l=-N}^N X_0(i+k, j+l) \cdot h_0(k) \cdot h_0(l)$$

The down sampler 23 down-samples the low-frequency image  $X_1$  output by the smoothing filter 20, thereby reducing the amount of image data. One simple method of down-sampling is to divide the image into blocks of  $M \times M$  pixels each, and replace each block by a representative pixel value such as the mean or median value in the block. This reduces the size of the image by a factor of  $M$  in both the horizontal and vertical directions, and reduces the amount of image data by a factor of  $M^2$ . The reduced image  $R$  output by the down sampler 23 resembles a photographic reduction of the original image  $X_0$ .

The low-frequency image encoder 25 encodes the reduced image R by, for example, performing a discrete cosine transform, quantizing the resulting DCT coefficients, and encoding them in zig-zag order by a variable-length encoding method. Alternatively, the reduced image R can be encoded by a predictive encoding method similar to the differential pulse-code modulation method (DPCM) commonly employed for audio signals, or by any other suitable method. If the above value of M is sufficiently large, high coding efficiency is not critically important, because the amount of data to be encoded has already been greatly reduced by down-sampling.

The edge detector 21 detects edges in the input image  $X_0$  by detecting variations in the rate of change of pixel values from pixel to pixel. More specifically, edge detector 21 takes differences between adjacent pixel values, then takes differences between these differences, and finally performs a thresholding operation to reduce small differences to zero. This process is carried out separately in the horizontal and vertical directions.

In the horizontal direction, for each pixel, the edge detector 21 computes the following pixel difference  $Pdh(i, j)$ :

$$Pdh(i, j) = X_0(i, j) - X_0(i - 1, j)$$

Next it computes differences  $Sh(i, j)$  of these pixel differences  $Pdh(i, j)$  as follows:

$$\begin{aligned} Sh(i, j) &= Pdh(i + 1, j) - Pdh(i, j) \\ &= X_0(i + 1, j) - 2X_0(i, j) + X_0(i - 1, j) \end{aligned}$$

Equivalent edge detection could be performed by filtering the image  $X_0$  with a second-derivative filter having coefficients (1, -2, 1), but taking differences of differences is preferable because it requires less computation.

The detected value  $Sh(i, j)$  is the edge sharpness at pixel (i, j), also referred to as the edge size. Edge sharpness values with absolute values equal to or less than a certain threshold value T are quantized to zero. Values exceeding T are also quantized, to reduce the number of bits in the edge-image data; then the quantized sharpness values  $Sh(i, j)$  at all pixels are output as the horizontal edge image Sh. Non-zero pixels in the edge image, i.e. pixels at which  $|Sh(i, j)| > T$ , are referred to as edge points.

The meaning of the edge sharpness computation can be seen in FIG. 3. The horizontal axis represents, for example, the horizontal direction in the image, and the vertical axis represents the pixel value. The difference between pixel values 38 and 39 is -A, the difference between pixel values 40 and 38 is B, and the difference between these differences is  $B - (-A) = A + B$ . If  $A + B > T$ , then pixel 38 is an edge point with sharpness  $A + B$ . The other pixels in FIG. 3 have zero sharpness. FIG. 3 thus illustrates a standard edge, with just one sharp variation.

FIG. 4 shows another standard edge, with non-zero sharpness at a single pixel 41. The size of the sharpness is  $\Theta$ , the difference to the left of pixel 41 being zero. FIGs. 3 and 4 are drawn so that  $\Theta = A + B$ , and edge points 38 and 41 have the same sharpness, or size.

In FIG. 5, pixel 42 is not an edge point. The differences on both sides of this pixel are the same, so the edge sharpness is zero. FIG. 5 represents an area in which, for example, intensity is shading from light to dark at a regular rate with no sharp change in this rate at any point. Incidentally, FIG. 5 represents the difference between FIGs. 3 and 4.

The vertical edge image is generated in the same way, by taking differences in the vertical direction, then taking differences of these differences. The edge sharpness  $Sv$  in the vertical direction is:

$$Sv(i, j) = X_0(i, j + 1) - 2X_0(i, j) + X_0(i, j - 1)$$

The vertical edge image is quantized as was the horizontal edge image, points for which  $|Sv(i, j)| \leq T$  being set to zero. Quantization can be performed by dividing the edge sharpness values by a power of two, for example, or by using a quantization table. Incidentally, the same notation Sh and Sv will be employed herein to denote edge sharpness values both before and after quantization.

The edge image encoder 24 encodes the quantized horizontal and vertical edge images Sh and Sv by using, for example, run-length encoding to encode the positions of edge points, and differential encoding to encode the sharpness values at these points. Alternatively, chain encoding can be used to encode the edge positions, taking advantage of the fact that edge points tend to be organized into chains of mutually contiguous points. Chain encoding encodes only the displacement from one position to the next in such a chain.

The encoded image C, consisting of the edge information  $C_s$  and low-frequency information  $C_r$  as multiplexed by the multiplexer 26, is output to the first input/output device 27 for transmission or storage. The amount of encoded data is generally reduced to a small fraction of the amount in the original image  $X_0$ , so the encoded image C can be efficiently transmitted or stored.

Next the decoding operations will be described. These operations employ further filters  $e_0$ ,  $f_0$ , and  $g_0$ .

Filter  $g_0$  is a high-pass filter having even symmetry and  $2N + 1$  taps, related to the smoothing filter  $h_0$  as

follows:

$$g_o(k) = (-1)^k h_o(-k)$$

This relation implies that the Fourier transform  $G_o$  of  $g_o$  is the complex conjugate of  $H_o(\omega + \pi)$ , so  $h_o$  and  $g_o$  satisfy the exact reconstruction condition and form a complementary pair. Furthermore,  $G_o(\omega + \pi)$  is the complex conjugate of  $H(\omega)$ , so  $g_o$  also satisfies an exact reconstruction condition by itself:

$$\begin{aligned} |H_o(\omega)|^2 + |G_o(\omega)|^2 &= 1 \\ |G_o(\omega)|^2 + |G_o(\omega + \pi)|^2 &= 1 \end{aligned}$$

Since  $|H_o(0)| = 1$ , it follows that  $|G_o(0)| = 0$ , so the sum of the tap coefficients  $g_o(k)$  is zero.

The above properties of  $g_o$  imply that when a uniform gradation of the type shown in FIG. 5 is filtered by  $g_o$ , the result will be uniformly zero. Consequently, if a standard edge of the type shown in FIG. 3 or 4 is filtered by  $g_o$ , the result will depend only on the edge sharpness. For example, FIGs. 3 and 4 give the same result when filtered by  $g_o$ . Moreover, this result has only  $2N - 1$  non-zero values, occurring in the vicinity of the edge point.

Filter  $e_o$  is an edge synthesis filter derived by filtering a standard edge of unit sharpness (for example, the edge in FIG. 4 with  $\Theta = 1$ ) by  $g_o$ . The result is:

$$e(t) = e(-t) = \sum_{k=t}^N (k - t) \cdot g_o(k) \quad \text{if } 0 \leq t < N$$

$$e(t) = e(-t) = 0 \quad \text{if } t \geq N$$

Filter  $f_o$  is a completion filter, defined in terms of its Fourier transform  $F_o$  as follows.

$$F_o(\omega) = \frac{1 + |H_o(\omega)|^2}{2}$$

Filter  $f_o$  is obtained by executing an inverse Fourier transform on  $F_o$ . Like  $h_o$  and  $g_o$ , filter  $f_o$  has even symmetry and  $2N + 1$  taps. The above definition of  $F_o$ , incidentally, is well known in the wavelet transform art and comes from the following equation:

$$|H(\omega)|^4 + 2F(\omega) \cdot |G(\omega)|^2 = 1$$

Referring again to FIG. 2, after the demultiplexer 30 separates the input encoded image C into edge information  $C_s$  and low-frequency information  $C_r$ , decoders 31 and 32 carry out processes that are reverse to the processes performed by encoders 24 and 25 in FIG. 1. For example, if the low-frequency image encoder 25 in FIG. 1 performed a discrete cosine transform followed by quantization and variable-length encoding, the low-frequency information decoder 32 in FIG. 2 performs variable-length decoding, dequantization, then an inverse discrete cosine transform to obtain the decoded reduced image  $R'$ .

If the edge images were encoded by run-length or chain encoding of the positions of the edge points and differential encoding of their sharpness, the edge information decoder 31 performs run-length or chain decoding to recover the edge positions and an accumulation process to obtain the sharpness values. The edge information decoder 31 also dequantizes the sharpness values.

The up sampler 35 obtains the decoded low-frequency image  $X_1$  by restoring the decoded reduced image  $R'$  to the original image size (the size of the edge images and high-frequency images). If, for example, each pixel value in the reduced image  $R$  represented an  $M \times M$  block in the low-frequency image  $X_1$ , the up sampler 35 can simply copy this value to all pixels in the corresponding  $M \times M$  block of the decoded low-frequency image  $X_1'$ . More sophisticated up-sampling methods involving interpolation can also be employed, but smooth interpolation is not required, because the image will be smoothed during the inverse wavelet transform.

The edge synthesizer 34 filters the horizontal edge image  $Sh$  by the edge synthesis filter  $e(t)$  in the horizontal direction, and the vertical edge image  $Sv$  by  $e(t)$  in the vertical direction. The resulting horizontal and vertical high-frequency images  $Yh$  and  $Yv$  can be described by the following equations:

$$Yh(i, j) = \sum_{t=1-N}^{N-1} Sh(i + t, j) \cdot e(t)$$

$$Yv(i, j) = \sum_{t=1-N}^{N-1} Sv(i, j + t) \cdot e(t)$$

Conceptually, for each edge point, the edge synthesizer 34 synthesizes the result of filtering a standard edge of the corresponding sharpness by the high-frequency filter  $g_o$ . It then adds these results together for all edge points to synthesize a high-frequency image.

The inverse wavelet transform processor 36 performs three two-dimensional filtering operations: it filters the decoded low-frequency image  $X_1'$  by  $h_o$  horizontally and vertically; filters the horizontal high-frequency image  $Y_h$  by  $g_o$  horizontally and  $f_o$  vertically; and filters the vertical high-frequency image  $Y_v$  by  $g_o$  vertically and  $f_o$  horizontally. The three results are added to obtain the reconstructed image  $X_0'$ . This inverse wavelet transform is well known in the art, and is described by the following equation:

$$\begin{aligned}
 X_0'(i, j) = & \sum_{k=-N}^N \sum_{l=-N}^N X_1'(i+k, j+l) \cdot h_o(k) \cdot h_o(l) \\
 & + \sum_{k=-N}^N \sum_{l=-N}^N Y_h(i+k, j+l) \cdot g_o(k) \cdot f_o(l) \\
 & + \sum_{k=-N}^N \sum_{l=-N}^N Y_v(i+k, j+l) \cdot g_o(l) \cdot f_o(k)
 \end{aligned}$$

The reconstructed image  $X_0'$  is output to the second input/output device 37 for display or print-out. Alternatively, the reconstructed image can be output to a computer for further image processing.

Synthesizing high-frequency image information from edge images as described above enables high compression ratios to be obtained. High-frequency information corresponding to edges below the sharpness threshold  $T$  is lost, so there is some loss of low-level, high-frequency shading variations, but the reconstructed image is not distorted, and sharp edges are faithfully preserved. Moreover, the high-frequency information is synthesized in a single, simple filtering step, without recourse to iterative procedures of uncertain reliability.

### Second embodiment

The second embodiment is similar to the first, but uses filters with an even instead of an odd number of taps, so the high-pass filter has odd symmetry, and the edge synthesis procedure is modified accordingly.

The digital image encoder of the second embodiment is identical to the digital image encoder in FIG. 1, except that the smoothing filter 20 employs a filter  $h_o$  with  $2N$  taps. This filter has even symmetry around  $1/2$ :

$$h_o(1-k) = h_o(k) \quad (k = 1, 2, \dots, N)$$

The Fourier transform  $H_o$  of this filter satisfies the same conditions as in the first embodiment:

$$\begin{aligned}
 |H_o(0)| &= 1 \\
 |H_o(\omega)|^2 + |H_o(\omega + \pi)|^2 &= 1
 \end{aligned}$$

The smoothing filter 20 obtains the low-frequency image  $X_1$  by filtering the input image  $X_0$  by  $h_o$  in the horizontal and vertical directions, as follows:

$$X_1(i, j) = \sum_{k=1-N}^N \sum_{l=1-N}^N X_0(i+k, j+l) \cdot h_o(k) \cdot h_o(l)$$

The digital image decoder is identical to the decoder in FIG. 2, except for the operation of the edge synthesizer 34 and inverse wavelet transform processor 36. The decoding description will be restricted to the operation of these elements.

The second embodiment employs a high-pass filter  $g_o$  with  $2N$  taps, obtained from the smoothing filter  $h_o$  by the same relation as in the first embodiment:

$$g_o(k) = (-1)^k h_o(-k) \quad (k = -N, \dots, N-1)$$

Whereas the filter  $g_o$  had even symmetry around zero,  $g_o$  has odd symmetry around  $-1/2$ :

$$g_o(-k) = -g_o(k-1) \quad (k = 1, 2, \dots, N)$$

As before, the Fourier transform  $G_o(\omega)$  of  $g_o$  is the complex conjugate of  $H_o(\omega + \pi)$ , and filters  $h_o$  and  $g_o$  form a complementary pair:

$$|H_e(\omega)|^2 + |G_e(\omega)|^2 = 1$$

The edge synthesizer 34 in the second embodiment filters the decoded edge images  $S_v$  and  $S_h$  by an edge synthesis filter obtained by filtering a standard edge with the high-pass filter  $g_e$ . This process could be carried out directly, as in the first embodiment, but in that case the following computational problem would arise.

When a uniform gradation as in FIG. 5 is filtered by  $g_e$ , the result has a constant value at every point, but this value is not in general zero. Accordingly, the results of filtering the standard edges in FIGs. 3 and 4 with  $g_e$  need not be the same. More significantly, their non-zero values will not necessarily be limited to the vicinity of the edge point, so direct filtering by an edge synthesis filter as in the first embodiment would require much computation.

If the differences between adjacent coefficient values in the edge synthesis filter are taken, however, these differences have the desirable properties of depending only on the edge sharpness, and being zero outside the vicinity of the edge point. Accordingly, the edge synthesizer 34 in the second embodiment is preferably configured as in FIG. 6, comprising a horizontal differential edge synthesizer 44, vertical differential edge synthesizer 45, horizontal accumulator 46, and vertical accumulator 47. The horizontal and vertical differential edge synthesizers 44 and 45 employ a differential edge synthesis filter with coefficients  $d_e(t)$  obtained by filtering a standard edge of unit sharpness with the high-pass filter  $g_e$ , then taking differences between the resulting values at adjacent pixels. Specifically,

$$d_e(t) = d_e(-t) = \sum_{k=t}^{N-1} g_e(k) \quad \text{if } 0 \leq t < N$$

$$d_e(t) = d_e(-t) = 0 \quad \text{if } t \geq N$$

Whereas the high-pass and low-pass filters  $g_e$  and  $h_e$  have an even number ( $2N$ ) of taps, the differential edge synthesis filter  $d_e$  has an odd number ( $2N - 1$ ) of taps.

The horizontal differential edge synthesizer 44 filters the horizontal edge image  $S_h$  horizontally by  $d_e(t)$  to obtain a horizontal differential high-frequency image  $\Delta Y_h$ . The vertical differential edge synthesizer 45 filters the vertical edge image  $S_v$  vertically with  $d_e(t)$  to obtain a vertical differential high-frequency image  $\Delta Y_v$ . These differential high-frequency images are described by the following equations:

$$\Delta Y_h(i, j) = \sum_{t=1-N}^{N-1} S_h(i + t, j) \cdot d_e(t)$$

$$\Delta Y_v(i, j) = \sum_{t=1-N}^{N-1} S_v(i, j + t) \cdot d_e(t)$$

The horizontal and vertical accumulators 46 and 47 then complete the edge synthesis filtering operation and generate the horizontal and vertical high-frequency images  $Y_h$  and  $Y_v$  by performing accumulation operations on  $\Delta Y_h$  and  $\Delta Y_v$  in the horizontal and vertical directions, respectively. If the horizontal and vertical coordinates  $i$  and  $j$  both start at zero, these accumulation operations can be described as follows:

$$Y_h(i, j) = \sum_{k=0}^i \Delta Y_h(k, j)$$

$$Y_v(i, j) = \sum_{k=0}^j \Delta Y_v(i, k)$$

The digital image decoder also employs a completion filter  $f_e$  defined by its Fourier transform  $F_e$  in the

same way as in the first embodiment:

$$F_o(\omega) = \frac{1 + |H_o(\omega)|^2}{2}$$

The inverse wavelet transform processor 36 in the second embodiment uses the conjugates  $f_o^*$ ,  $g_o^*$ , and  $h_o^*$  of filters  $f_o$ ,  $g_o$ , and  $h_o$ , defined as explained earlier, e.g.  $h_o^*(-k) = h_o(k)$ . The inverse wavelet transform processor 36 filters the decoded low-frequency image  $X_1'$  by  $h_o^*$  horizontally and vertically, filters the horizontal high-frequency image  $Y_h$  by  $g_o^*$  horizontally and  $f_o^*$  vertically, filters the vertical high-frequency image  $Y_v$  by  $g_o^*$  vertically and  $f_o^*$  horizontally, and adds the three results to obtain the reconstructed image  $X_0'$ , as follows:

$$\begin{aligned} X_0'(i, j) = & \sum_{k=-N}^{N-1} \sum_{l=-N}^{N-1} X_1'(i+k, j+l) \cdot h_o^*(k) \cdot h_o^*(l) \\ & + \sum_{k=1-N}^N \sum_{l=-N}^{N-1} Y_h(i+k, j+l) \cdot g_o^*(k) \cdot f_o^*(l) \\ & + \sum_{k=-N}^{N-1} \sum_{l=1-N}^N Y_v(i+k, j+l) \cdot g_o^*(l) \cdot f_o^*(k) \end{aligned}$$

The inverse wavelet transforms employed in the first and second embodiments are essentially the same, because the even filters employed in the first embodiment are equal to their own conjugates.

The difference being using filters with an odd number of taps as in the first embodiment and an even number of taps as in the second embodiment is mainly computational. In terms of image encoding and reconstruction, both embodiments provide similar effects.

### Third embodiment

The third embodiment is similar to the first embodiment, but the smoothing and inverse wavelet transform operations are cascaded over  $n$  wavelet scales, where  $n$  is an arbitrary integer greater than one, with edge detection and edge synthesis performed at each scale.

Referring to FIG. 7, the digital image encoder in the third embodiment comprises  $n$  cascaded smoothing filters 20-1 to 20- $n$ ,  $n$  edge detectors 21-1 to 21- $n$ , and a selector 50. It also comprises the same encoding section 22 and down sampler 23 as in the first embodiment in FIG. 1, the encoding section 22 having the same encoders 24 and 25 and multiplexer 26.

The image  $X_0$  input at the input terminal 10 is supplied to the first smoothing filter 20-1 and edge detector 21-1. Each smoothing filter 20- $m$  in the cascade ( $m = 1$  to  $n-1$ ) outputs a filtered image  $X_m$  to the next smoothing filter 20- $(m+1)$  and next edge detector 21- $(m+1)$ . The last smoothing filter 20- $n$  outputs a low-frequency image  $X_n$  to the down sampler 23.

The edge detectors 21- $m$  are all identical to the edge detector 21 in FIG. 1. Each produces a pair of horizontal and vertical edge images  $Sh_m$  and  $Sv_m$ , which are input to the selector 50. The selector 50 selects one of these pairs of edge images, and supplies the selected pair  $Sh$  and  $Sv$  to the edge image encoder 24.

Referring to FIG. 8, the digital image decoder in the third embodiment comprises the same decoding section 28 and up sampler 35 as in the preceding embodiments, the decoding section 28 comprising the same demultiplexer 30 and decoders 31 and 32. The decoded low-frequency image output by the up sampler 35 is now denoted  $X_n'$ . The digital image decoder also comprises a distributor 51,  $n$  edge synthesizers 34- $m$  ( $m = 1$  to  $n$ ), and an inverse wavelet transform processor 36. The inverse wavelet transform processor 36 now consists of  $n$  cascaded inverse wavelet processors 36- $m$ .

The distributor 51 distributes identical copies of the horizontal and vertical edge images  $Sh$  and  $Sv$  received from the edge information decoder 31 to all of the edge synthesizers 34- $m$  ( $m = 1$  to  $n$ ). Each edge synthesizer 34- $m$  outputs a pair of horizontal and vertical high-frequency images  $Y_{h_m}$  and  $Y_{v_m}$  to the corresponding inverse wavelet processor 36- $m$ . Each inverse wavelet processor 36- $m$  ( $m = 1$  to  $n-1$ ) also receives a partially reconstructed image  $X_m$  from the preceding inverse wavelet processor 36- $(m+1)$  in the cascade, and outputs a partially reconstructed image  $X_{m-1}$  to the next inverse wavelet processor 36- $(m-1)$ . The first inverse wavelet processor 36- $n$  in the cascade receives the decoded low-frequency image  $X_n'$  from the up sampler 35. The last inverse wavelet processor 36-1 outputs the fully reconstructed image  $X_0'$  to the input/output device 37.

Next the operation of the third embodiment will be described, starting with the encoding operation.

Each of the smoothing filters 20-m in FIG. 7 employs a different set of filter coefficients  $h_{om}(k)$ . All of these filters have properties similar to the filter  $h_o$  employed in the first embodiment: even symmetry, and an odd number of taps with coefficients summing to plus or minus one. The first filter  $h_{o1}$  satisfies the exact reconstruction condition.

The cut-off frequency of each smoothing filter 20-m ( $m > 1$ ) is one-half the cut-off frequency of the preceding filter 20-( $m - 1$ ) in the cascade. Filters with these cut-off frequencies can be obtained by doubling the tap spacing from one filter to the next. That is,  $h_{om}$  can be obtained from  $h_{o(m-1)}$  as follows:

$$\begin{aligned} h_{om}(2k) &= h_{o(m-1)}(k) \\ h_{om}(2k+1) &= 0 \quad (k = 0, \pm 1, \pm 2, \dots) \end{aligned}$$

In smoothing filter 20-m, image  $X_{m-1}$  is filtered horizontally and vertically by filter  $h_{om}$  to obtain image  $X_m$ . As the image passes through the cascade, variations on progressively larger scales are smoothed out, and resolution of fine detail decreases correspondingly.

Incidentally, if the low-pass filters  $h_{om}$  ( $m > 1$ ) are obtained by doubling the tap spacing as described above, they will pass high-frequency variations in certain regions above their cut-off frequencies. This is allowable because those high-frequency variations will already have been removed by previous filters in the cascade.

Each edge detector 21-m detects edges in the received image  $X_{m-1}$  by the difference-of-differences method described in the first embodiment. All of the edge detectors 21-m can use the same threshold  $T$ , or different thresholds can be employed in different edge detectors.

In selecting one of the pairs of horizontal and vertical edge images  $Sh_m$  and  $Sv_m$  ( $m = 1$  to  $n$ ) produced by the edge detectors 21-m, the selector 50 may select according to statistics of the edge images themselves, such as the number of edge points detected, or according to an external command specifying a desired edge resolution. In general, edge images  $Sh_m$  and  $Sv_m$  obtained at lower resolutions (higher values of  $m$ ) contain less information, but also contain less noise.

The operations of down-sampling, encoding the selected edge images  $Sh$  and  $Sv$ , encoding the reduced image  $R$ , and multiplexing the encoded data are carried out as in the first embodiment.

Next the decoding operations will be described. These operations employ edge synthesis filters  $e_{om}$ , completion filters  $f_{om}$ , and high-pass filters  $g_{om}$  ( $m = 1$  to  $n$ ). Filters  $f_{om}$  and  $g_{om}$  are related to respective smoothing filters  $h_{om}$  ( $m = 1$  to  $n$ ) in the same way that  $f_o$  and  $g_o$  were related to  $h_o$  in the first embodiment. Specifically,

$$\begin{aligned} |H_{om}(\omega)|^2 + |G_{om}(\omega)|^2 &= 1 \\ F_{om}(\omega) &= \frac{1 + |H_{om}(\omega)|^2}{2} \end{aligned}$$

Once  $f_{o1}$  and  $g_{o1}$  have been obtained from  $h_{o1}$ , the other filters  $f_{om}$  and  $g_{om}$  ( $m = 2$  to  $n$ ) can be obtained by successively doubling the tap spacing, as was done for the low-pass filters  $h_{om}$ , so that the number of taps remains  $2N + 1$  in each filter.

Each edge synthesis filter  $e_{om}$  is obtained by filtering a standard edge of unit sharpness with the corresponding high-pass filter  $g_{om}$ . The length of each edge synthesis filter  $e_{om}$  (the distance between the two outermost taps) is still less by two than the length of the corresponding high-pass filter  $g_{om}$ , but  $e_{om}$  may have more taps than  $g_{om}$ , because while  $g_{om}$  may have many zero coefficients between the outermost two taps,  $e_{om}$  in general will not.

The operations of demultiplexing the encoded image  $C$ , decoding the edge information  $Cs$ , and decoding and up-sampling the low-frequency information  $Cr$  are carried out as in the first embodiment, producing a pair of edge images  $Sh$  and  $Sv$  and a decoded low-frequency image  $X_n'$ .

Each edge synthesizer 34-m ( $m = 1$  to  $n$ ) filters the pair of edge images  $Sh$  and  $Sv$  by its edge synthesis filter  $e_{om}$  to generate horizontal and vertical high-frequency images  $Yh_m$  and  $Yv_m$ . Even though all the edges were detected at a single selected resolution, the edge synthesizers 34-m synthesize high-frequency images at different resolutions from these same edges.

Each inverse wavelet processor 36-m generates the image  $X_{m-1}'$  from images  $X_m$ ,  $Yh_m$ , and  $Yv_m$  in the same way that the inverse wavelet transform processor 36 in the first embodiment generated  $X_0$  from  $X_1$ ,  $Yh$ , and  $Yv$ . That is, it filters  $X_m'$  horizontally and vertically by  $h_{om}$ , filters  $Yh_m$  horizontally by  $g_{om}$  and vertically by  $f_{om}$ , filters  $Yv_m$  vertically by  $g_{om}$  and horizontally by  $f_{om}$ , and sums the three results.

The compression ratios achieved by the third embodiment are comparable to those achieved by the first embodiment because the encoded information again comprises just one reduced low-frequency image and two edge images, but the third embodiment gives better reconstruction of images with high-frequency noise. In the first embodiment, such noise can only be dealt with by raising the edge sharpness threshold  $T$ , which is not always a satisfactory solution. In the third embodiment, edges can be detected in an image from which high-frequency noise has been removed by smoothing, so it is not necessary to raise the edge sharpness

threshold.

#### Fourth embodiment

5 The fourth embodiment differs from the third embodiment in that the digital image encoder selects a particular resolution before performing edge detection. The digital image decoder is the same as in the third embodiment.

Referring to FIG. 9, the digital image encoder has smoothing filters 20-m ( $m = 1$  to  $n$ ) that output filtered images  $X_m$  as in the third embodiment. A selector 52 selects one of these images for input to an edge detector 21, which is identical to the edge detector 21 in the first embodiment. Other elements in FIG. 9 are also identical to corresponding elements in the first and third embodiments, and have the same reference numerals.

The selector 52 selects a particular image  $X_m$  according to statistics taken from the images  $X_m$  ( $m = 1$  to  $n$ ), or according to an external command. The edge detector 21 detects edges in the selected image as in the first embodiment. Other than this difference, the fourth embodiment operates like the third embodiment, so further description will be omitted.

The fourth embodiment has the advantage of a simpler configuration than the third embodiment, and needless computation of edge images that will not be used is avoided. The selector 52 must, however, select a resolution before seeing the results of edge detection.

#### Fifth embodiment

The fifth embodiment differs from the third embodiment in using filters with an even number of taps.

Referring to FIG. 10, the digital image encoder of the fifth embodiment has the same structure as in the third embodiment, and the same reference numerals are used as in FIG. 7 except for the cascaded smoothing filters 54-m, which employ different filter functions  $h_{em}$  ( $m = 1$  to  $n$ ).

The filters  $h_{em}$  are similar to the filter  $h_0$  in the second embodiment in being low-pass filters with even symmetry and an even number of taps. As in the third embodiment, each filter  $h_{em}$  can be obtained from the preceding filter by doubling the spacing between taps, which reduces the cut-off frequency by a factor of two.

Other than the difference in the number of taps, the operation of the digital image encoders in the third and fifth embodiments is the same.

Referring to FIG. 11, the digital image decoder in the fifth embodiment is similar to the digital image decoder of the third embodiment (FIG. 8), except that the high-frequency images are generated by the method of FIG. 6. Elements that have the same reference numerals in FIGs. 11 and 8 are identical.

The distributor 51 in the fifth embodiment distributes the pair of edge images  $Sh$  and  $Sv$  to pairs of differential edge synthesizers 56-m and 57-m ( $m = 1$  to  $n$ ), which are similar to the differential edge synthesizers 44 and 45 in FIG. 6. These output differential high-frequency images  $\Delta Yh_m$  and  $\Delta Yv_m$  to corresponding pairs of accumulators 60-m and 61-m, which are similar to the accumulators 46 and 47 in FIG. 6. The accumulators 60-m and 61-m output high-frequency images  $Yh_m$  and  $Yv_m$  to the inverse wavelet transform processor 36. The inverse wavelet transform processor 36 comprises inverse wavelet processors 62-n ( $m = 1$  to  $n$ ) which are similar to the inverse wavelet processors 36-n in the third embodiment, except that they employ filters  $f_{em}^*$ ,  $g_{em}^*$ , and  $h_{em}^*$  with an even number of taps.

Filter  $h_{em}^*$  is conjugate to the low-pass filter  $h_{em}$  employed in the encoder. Filters  $f_{em}^*$  and  $g_{em}^*$  are related to  $h_{em}^*$  in the same way that  $f_0$  and  $g_0$  were related to  $h_0$  in the first embodiment. Specifically, the Fourier transforms  $F_{em}^*$ ,  $G_{em}^*$ , and  $H_{em}^*$  of these filters satisfy:

$$45 \quad |H_{em}^*(\omega)|^2 + |G_{em}^*(\omega)|^2 = 1$$

$$F_{em}^*(\omega) = \frac{1 + |H_{em}^*(\omega)|^2}{2}$$

Filters  $f_{em}^*$  can be obtained from the above definitions of their Fourier transforms. Filters  $g_{em}^*$  can be constructed as follows.

First, filters  $f_{e1}$  and  $g_{e1}$  are obtained from smoothing filter  $h_{e1}$  in the same way that  $f_0$  and  $g_0$  were obtained from  $h_0$  in the second embodiment:

$$g_{e1}(k) = (-1)^k h_{e1}(-k) \quad (k = -N \dots, N-1)$$

Each filter  $g_{em}$  ( $m = 2$  to  $n$ ) is then obtained from the preceding filter  $g_{e(m-1)}$  by doubling the tap spacing. Each filter  $g_{em}$  ( $m = 1$  to  $n$ ) has odd symmetry. The order of taps of  $g_{em}$  can be reversed to obtain the conjugate  $g_{em}^*$ .

The differential edge synthesizers 56-m and 57-m ( $m = 1$  to  $n$ ) employ respective differential edge synthesis filters  $d_{em}$  that are obtained from the above high-pass filters  $g_{em}$  in the same way that the filter  $d_e$  was obtained from  $g_0$  in the second embodiment: by filtering a standard edge of unit sharpness with  $g_{em}$ , then taking

the differences between resulting coefficients.

The decoding section 28, up sampler 35, and distributor 51 operate as in the third embodiment. The differential edge synthesizers 56-m and 57-m ( $m = 1$  to  $n$ ) operate like the differential edge synthesizers 44 and 45 in the second embodiment, filtering the pair of edge images  $Sh$  and  $Sv$  by their respective differential edge synthesis filters  $de_m$ . The accumulators 60-m and 61-m operate like the accumulators 46 and 47 in FIG. 6.

The inverse wavelet transform processor 36 operates as in the third embodiment. Each inverse wavelet processor 62-m generates image  $X_{m-1}'$  from images  $X_m'$ ,  $Yh_m$ , and  $Yv_m$  by filtering  $X_m'$  horizontally and vertically with  $h_{em}^*$ , filtering  $Yh_m$  horizontally with  $g_{em}^*$  and vertically with  $f_{em}^*$ , filtering  $Yv_m$  vertically with  $g_{em}^*$  and horizontally with  $f_{em}^*$ , and summing the three results.

The fifth embodiment has effects similar to the third embodiment.

#### Sixth embodiment

The sixth embodiment differs from the fifth as the fourth embodiment differed from the third: the digital image encoder selects a particular resolution before performing edge detection. The digital image decoder is the same as in the fifth embodiment.

Referring to FIG. 12, the digital image encoder has the same smoothing filters 54-m ( $m = 1$  to  $n$ ) as in the fifth embodiment, which output filtered images  $X_m$ . As in the fourth embodiment, a selector 52 selects one of the images  $X_m$  ( $m = 0$  to  $n - 1$ ). The other elements in FIG. 12 also operate as in the fourth embodiment, being identical to elements in 9 with the same reference numerals.

Aside from the use of filters with an even number of taps, the sixth embodiment operates exactly like the fourth embodiment and provides the same effects, so further description will be omitted.

#### Seventh embodiment

The seventh embodiment is similar to the third embodiment, but is adapted to process moving images comprising successive frames.

Referring to FIG. 13, the digital image encoder of the seventh embodiment has the same smoothing filters 20-m and edge detectors 21-m ( $m = 1$  to  $n$ ) as the third embodiment, and the same down sampler 23 and selector 50. The encoding section 65, however, differs from the encoding section 22 in the third embodiment.

The encoding section 65 comprises a pair of motion estimators 66 and 67, a motion vector encoder 68, a pair of difference encoders 69 and 73, a pair of difference decoders 70 and 74, a pair of adders 71 and 75, and a pair of memories 72 and 76.

Motion estimator 66 receives the pair of edge images  $Sh$  and  $Sv$  from selector 50, and corresponding previous edge images  $Sp$  from memory 72. The symbol  $Sp$  in the drawing denotes a pair of images: a previous horizontal edge image and a previous vertical edge image. Motion estimator 66 outputs edge motion vectors  $Ms$  and a pair of horizontal and vertical edge difference images  $Dh$  and  $Dv$ . Motion estimator 67 receives the reduced image  $R$  from down sampler 23 and a previous reduced image  $Rp$  from memory 76, and outputs low-frequency motion vectors  $Mr$  and a reduced difference image  $Dr$ . The motion vector encoder 68 encodes motion vectors  $Ms$  and  $Mr$  and outputs motion information  $Cm$ .

Difference encoder 69 encodes the edge difference images  $Dh$  and  $Dv$  and outputs edge difference information  $Cds$ . This information is decoded by difference decoder 70 to produce a pair of decoded edge difference images, which are denoted in the drawing by the single symbol  $D'$ . Similarly, difference encoder 70 encodes the reduced difference image  $Dr$  and outputs low-frequency difference information  $Cdr$ . Difference decoder 74 decodes  $Cdr$  and outputs a decoded reduced difference image  $Dr'$ .

The multiplexer 78 multiplexes the edge difference information  $Cds$ , motion information  $Cm$ , and low-frequency difference information  $Cdr$  for output as an encoded image  $C$ .

Referring to FIG. 14, the digital image decoder of the seventh embodiment is identical to the digital image decoder of the third embodiment except for the decoding section 79. The decoding section 79 comprises a demultiplexer 80, a motion information decoder 82, a pair of difference decoders 81 and 83, a pair of motion compensators 84 and 86, and a pair of memories 85 and 87.

The demultiplexer 80 receives the encoded image  $C$  from the input/output device 27 and separates it into edge difference information  $Cds$ , motion information  $Cm$ , and low-frequency difference information  $Cdr$ . Difference decoder 81 decodes  $Cds$  to obtain a pair of decoded edge difference images  $Dh'$  and  $Dv'$ . Motion information decoder 82 decodes  $Cm$  to obtain edge motion vectors  $Ms$  and low-frequency motion vectors  $Mr$ . Difference decoder 83 decodes  $Cdr$  to obtain a decoded reduced difference image  $Dr'$ .

Motion compensator 84 receives  $Dh'$ ,  $Dv'$  and  $Ms$  from decoders 81 and 82, receives a pair of previous

edge images  $S_p$  from memory 85, and outputs a pair of edge images  $S_h$  and  $S_v$  to the distributor 51. Motion compensator 86 receives  $M_r$  and  $D_r'$  from decoders 82 and 83, receives a previous reduced image  $R_p'$  from memory 87, and outputs a decoded reduced image  $R'$  to the up sampler 35.

Next the operation of the seventh embodiment will be described. The description will be confined to the encoding section 65 and decoding section 79, since the other parts operate as in the third embodiment.

Referring again to FIG. 13, motion estimator 67 estimates motion in the reduced image  $R$  by, for example, the block-matching method, which involves computing the following quantity  $d(k, l)$  for blocks of pixels in the reduced image  $R$  and previous reduced image  $R_p$ , using various vectors  $(k, l)$ .

$$d(k, l) = \sum_{i, j} |R(i, j) - R_p(i + k, j + l)|$$

That is, motion estimator 67 finds the total difference in pixel values between a block in the current reduced image  $R$  and a block displaced by  $k$  pixels horizontally and  $l$  pixels vertically from this block in the previous reduced image  $R_p$ . This computation is carried out for all vectors  $(k, l)$  within a designated search range; then the minimum resulting value  $d(k, l)$  is selected and its vector  $(k, l)$  is output as a motion vector. The entire procedure is repeated for other blocks in the current reduced image, until motion has been estimated throughout the image, thereby obtaining the low-frequency motion vectors  $M_r$ .

The reduced difference image  $D_r$  consists of the differences in pixel values between the current and previous reduced images that cannot be accounted for by the detected motion. That is, it consists of values of the form

$$D_r(i, j) = R(i, j) - R_p(i + k, j + l)$$

where  $(k, l)$  is the motion vector of the block containing pixel  $R(i, j)$ .

Motion estimator 66 carries out this block-matching process separately on the horizontal and vertical edge images  $S_h$  and  $S_v$ . The motion vector encoder 68 encodes the motion vectors  $M_s$  and  $M_r$  by, for example, a variable-length encoding method.

Difference decoder 69 encodes the edge difference images  $D_h$  and  $D_v$  by, for example, the same method as used by the edge image encoder 24 in the preceding embodiments. Difference encoder 74 encodes the reduced difference image by, for example, the same method as used by the low-frequency image encoder 25 in the preceding embodiments. The difference decoders 70 and 74 employ methods reverse to those of the difference encoders 69 and 73 to obtain the decoded reduced difference image  $D_r'$  and the pair of decoded edge difference images  $D'$ .

Adder 75 adds  $D_r'$  to the previous reduced image  $R_p$  and stores the resulting new reduced image in memory 76 for use as a previous reduced image in the next frame. Similarly, adder 71 adds the pair of decoded edge difference images  $D'$  to the previous pair of edge images  $S_p$  and stores the resulting pair of new edge images in memory 72 for use as previous edge images in the next frame.

Memory 76 has space for storing at least two complete reduced images, the previous image read by motion estimator 67 and the new image stored by adder 75 being stored in different memory locations. In one storage scheme the roles of these locations are swapped at every frame, so what was the new image in one frame becomes the previous image in the next frame. Memory 72 can employ a similar scheme for storing edge images.

Block matching requires a large number of arithmetic operations, particularly in motion estimator 66, which must estimate motion in two full-size images  $S_h$  and  $S_v$ . Motion estimator 66 is preferably adapted to reduce the amount of computation by restricting the search range. For example, motion in a frame can to some extent be predicted from motion in preceding frames, and the search range can be narrowed on the basis of these predictions, a technique well known in the prior art. Also, after motion vectors for one of the two edge images  $S_h$  and  $S_v$  have been obtained, they can be used to narrow the search range for motion vectors in the other edge image. In the extreme case, block matching can be performed for just one of the two edge images, and the resulting motion vectors applied to both edge images.

Needless to say, the motion-vector search for a block terminates as soon as a vector  $(k, l)$  with  $d(k, l) = 0$  is found. This tends to happen quickly for many blocks in the edge images, because most pixel values in an edge image are usually zero. That is, edge detection in itself generally reduces the amount of motion-vector computation.

Referring again to FIG. 14, to describe the operation of decoding section 79, after the demultiplexer 80 separates the encoded image signal  $C$  into  $C_d$ ,  $C_m$ , and  $C_r$ , the difference decoder 81 decodes  $C_d$  to obtain decoded edge difference images  $D_h'$  and  $D_v'$ . These images are identical to the decoded edge difference images obtained by the difference decoder 70 in the digital image encoder and denoted  $D'$  in FIG. 13.

The motion information decoder 82 decodes  $C_m$  to obtain decoded motion vectors  $M_s$  and  $M_r$ . The motion vector encoding scheme is preferably a lossless encoding scheme, so these vectors  $M_s$  and  $M_r$  are identical to the motion vectors  $M_s$  and  $M_r$  in FIG. 13. The difference decoder 83 decodes  $C_{dr}$  to obtain a decoded reduced image  $D_r'$  which is identical to the decoded reduced image  $D_r'$  obtained by difference decoder 74 in FIG. 13.

For each block of difference values in the decoded reduced difference image  $D_r'$ , motion compensator 86 obtains the motion vector  $(k, l)$  of the block from the motion information decoder 82, reads a previous block displaced by  $(k, l)$  in the previous image  $R_p$  stored in memory 87, and adds the difference values to this previous block to obtain a block in the decoded reduced image  $R'$ . After obtaining the complete reduced image  $R'$ , it stores this newly decoded reduced image  $R'$  in memory 87 for use as the previous reduced image  $R_p$  in the next frame. Operating in a similar fashion, motion compensator 84 obtains horizontal and vertical edge images  $S_h$  and  $S_v$  from the decoded edge difference images  $D_h'$  and  $D_v'$ , motion vectors  $M_s$ , and previous edge images  $S_p$ , and stores the new edge images  $S_h$  and  $S_v$  in memory 85 for use as previous edge images in the next frame. Memories 85 and 87 employ storage schemes similar to those of memories 72 and 76 in the digital image encoder.

Due to quantization error, for example, the decoded difference images  $D_h'$ ,  $D_v'$ , and  $D_r'$  obtained by difference decoders 81 and 83 in the digital image decoder will not necessarily be identical to the difference images  $D_h$ ,  $D_v$ , and  $D_r$  output by motion estimators 66 and 67 in the digital image encoder. They will, however, be identical to the decoded difference images obtained by difference decoders 70 and 74 in the digital image encoder, as noted above, so the new images stored in memories 85 and 87 in the digital image decoder will be identical to the new images stored in memories 72 and 76 in the digital image encoder.

Accordingly, the motion estimators 66 and 67 and motion compensators 84 and 86 will always estimate and compensate for motion from the same previous images, even if the difference images are not decoded with perfect accuracy.

The combination of edge detection, down-sampling, and motion estimation and compensation enables very high video compression ratios to be achieved, particularly for images that tend to remain largely stationary. Such images include the images commonly encountered in video teleconferencing, video games, and a wide variety of so-called multimedia systems, which provide a video interface between a person and a computer. The present invention can be practiced to good advantage in systems like these.

#### Eighth embodiment

The eighth embodiment combines features of the fourth and seventh embodiments.

Referring to FIG. 15, the digital image encoder of the eighth embodiment has the same smoothing filters 20-1 to 20-n, edge detector 21, down sampler 23, and selector 52 as the fourth embodiment (FIG. 9), and the same motion-estimating encoding section 65 as the seventh embodiment (FIG. 13). The digital image decoder of the eighth embodiment is identical to the digital image decoder of the seventh embodiment (FIG. 14).

The eighth embodiment operates as explained in the fourth and seventh embodiments, so further description will be omitted. By reducing the amount of edge detection, the eighth embodiment speeds up the encoding of moving images and enables frames to be encoded at a faster rate than the seventh embodiment.

#### Ninth embodiment

The ninth embodiment is similar to the eighth embodiment, with further provisions for speeding up the computation of motion vectors.

Referring to FIG. 16, the encoding section 89 of the digital image encoder of the ninth embodiment is similar to the encoding section 65 in FIGs. 13 and 15, but has an additional bidirectional interconnection 90 between motion estimators 66 and 67. This enables motion estimator 67 to receive the edge motion vectors  $M_s$  found by motion estimator 66, scale them down to the size of the reduced image  $R$ , and restrict the search for motion vectors in the reduced image  $R$  to vectors close to the scaled-down edge motion vectors. In the extreme case, the search can be eliminated entirely: the low-frequency motion vectors  $M_r$  can be computed directly from the edge motion vectors  $M_s$ .

Alternatively, motion estimator 66 can receive the motion vectors  $M_r$  found by block matching in the reduced image  $R$ , scale them up to the size of the edge images  $S_h$  and  $S_v$ , and restrict the search for motion vectors for  $S_h$  and  $S_v$  to vectors close to the scaled-up vectors. This scheme is particularly effective in detecting sudden, large motion.

Aside from this difference, the ninth embodiment has the same structure as the eighth embodiment and operates in the same way, so further description will be omitted. By narrowing down the search for motion vectors, the ninth embodiment enables frames to be encoded at a still faster rate than the eighth embodiment.

Tenth embodiment

The tenth embodiment combines features of the fifth and seventh embodiments.

Referring to FIG. 17, the digital image encoder of the tenth embodiment has the same smoothing filters 54-1 to 54-n, edge detectors 21-1 to 21-n, down sampler 23, and selector 50 as the fifth embodiment (FIG. 10), and the same encoding section 65 as the seventh embodiment (FIG. 13). Referring to FIG. 18, the digital image decoder of the tenth embodiment has the same decoding section 79 as the seventh embodiment (FIG. 14), and the same up sampler 35, distributor 51, differential edge synthesizers 56-1 to 57-n, accumulators 60-1 to 61-n, and inverse wavelet transform processor 36 as the fifth embodiment (FIG. 11). The tenth embodiment operates as explained in the fifth and seventh embodiments, and provides similar effects, so further description will be omitted.

Eleventh embodiment

The eleventh embodiment combines features of the sixth and seventh embodiments.

Referring to FIG. 19, the digital image encoder of the eleventh embodiment has the same smoothing filters 54-1 to 54-n, edge detector 21, down sampler 23, and selector 52 as the sixth embodiment (FIG. 12), and the same encoding section 65 as the seventh embodiment (FIG. 13). The digital image decoder of the eleventh embodiment is identical to the digital image decoder of the seventh embodiment (FIG. 14). The eleventh embodiment operates as explained in the sixth and seventh embodiments, and provides similar effects, so further description will be omitted.

Twelfth embodiment

The twelfth embodiment combines features of the ninth and eleventh embodiments.

Referring to FIG. 20, the digital image encoder of the twelfth embodiment has the same smoothing filters 54-1 to 54-n, edge detector 21, down sampler 23, and selector 52 as the eleventh embodiment (FIG. 18), and the same encoding section 89 as in the ninth embodiment (FIG. 16), with a bidirectional interconnection 90 between the motion estimators 66 and 67. The digital image decoder of the eleventh embodiment is identical to the digital image decoder of the tenth and eleventh embodiments (FIG. 18). The twelfth embodiment operates as explained in these preceding embodiments, and provides similar effects, so further description will be omitted.

As described above, the invented encoding and decoding method and devices provide high compression rates with modest amounts of computation. Although there is some loss of detail, reconstructed images are not distorted, and because high-frequency information is synthesized in a single step instead of by iteration, the reconstruction process is reliably completed within a short and definite time. The invention can be usefully applied to both still and moving images for a wide variety of purposes, in a wide variety of systems.

The scope of the invention is not restricted to the embodiments described above, but includes such further variations as the following.

Instead of producing separate horizontal and vertical edge images, the edge detector or detectors can output a single edge image in which each edge point has both a sharpness value  $S(i, j)$  and direction angle  $\theta(i, j)$ , computed from  $Sh(i, j)$  and  $Sv(i, j)$  by, for example, the following formulas:

$$S(i, j) = \text{sgn}[Sh(i, j)] \cdot \{Sh(i, j)^2 + Sv(i, j)^2\}^{1/2}$$

$$\theta(i, j) = \arctan[Sv(i, j)/Sh(i, j)]$$

In these formulas,  $\text{sgn}$  denotes the algebraic sign (plus or minus one), and  $\arctan$  the inverse tangent function. The encoding section then has only one set of edge positions to encode. The decoding section in the digital image decoder can recover  $Sh(i, j)$  and  $Sv(i, j)$  from  $S(i, j)$  and  $\theta(i, j)$ .

This method of encoding only a single edge image is particularly effective if chain encoding is employed, because the direction angle tends to change only slowly along a chain of edge points. Moreover, the angle tends to be perpendicular to the direction of the chain, so it is possible to have the edge detector calculate only the above edge sharpness  $S(i, j)$ , and let the decoder infer the direction angle  $\theta(i, j)$  of an edge point  $(i, j)$  from the direction of the chain in the vicinity of that point. The compression ratio can be significantly improved in this way, although with some loss of reconstruction accuracy.

If the seventh to twelfth embodiments are varied in this way, the new and previous edge images  $S_p$  stored in memories 72 and 85 can be a single edge image with pixel values  $S(i, j)$ . After storing a single new edge image in memory 85, motion compensator 84 infers  $\theta(i, j)$ , calculates  $Sh(i, j)$  and  $Sv(i, j)$ , and generates horizontal and vertical edge images  $Sh$  and  $Sv$  for output to the distributor 51.

The number of smoothing filters need not be the same as the number of edge synthesizers. If the desired

edge resolution is known in advance, the digital image encoder requires no more than two smoothing filters: one to smooth the input image to the desired resolution for edge detection, and another to obtain the low-frequency image by further smoothing. Despite this, the digital image decoder may employ an arbitrary number of edge synthesizers for reconstruction. In this case it is not necessary for the low-pass filters in the digital image encoder and high-pass filters in the digital image decoder to form complementary pairs.

Obtaining a series of low-pass or high-pass filters by successive doubling of the tap spacing is computationally convenient, but the low-pass and high-pass filters do not have to be obtained in this way, and their cut-off frequencies do not have to be related by factors of one-half. Moreover, the low-pass filters in the digital image encoder do not have to be cascaded; a set of parallel low-pass filters could be used.

Since the difference-of-differences edge detection method is equivalent to filtering by a second-derivative filter  $d$  with coefficients  $(1, -2, 1)$ , the encoder could also be structured so that the input image is filtered by a cascaded series of filters comprising  $d, h_1, h_2, \dots, h_{n-1}$  to detect edge points at various resolutions. The input image can then be separately filtered by a single filter equal to the convolution of  $h_1, h_2, \dots, h_n$  to obtain the low-frequency image ( $h_1, \dots, h_n$  are low-pass filters).

Edge detection is not limited to the difference-of-differences method. This method is preferred, but other methods can be employed.

The standard edge from which the edge synthesis filters are generated need not be an edge of the type shown in FIGs. 3 and 4, in which pixel values change at first one constant rate, then another constant rate. Other types can be used, provided the standard edge has only one point which would be detected as an edge point by the edge detector.

Down-sampling and up-sampling can be omitted, if the encoding method employed for the low-frequency image provides adequate data compression without down-sampling.

In the seventh to twelfth embodiments, if the difference images  $D_h, D_v$ , and  $D_r$  produced by motion estimation are encoded by a lossless encoding scheme, memories 72 and 76 in the digital image encoders can be updated by direct input of edge images  $S_h$  and  $S_v$  and reduced image  $R$ . Difference decoders 70 and 74 and adders 71 and 75 can then be omitted.

Motion estimation and compensation need not be performed in every frame. These processes can be omitted occasionally, to prevent unchecked propagation of errors from frame to frame.

Motion estimation and compensation can of course be added to the one-stage digital image encoders and decoders of the first two embodiments, to compress moving images.

Those skilled in the art will recognize that still further variations are possible without departing from the scope claimed below.

## Claims

1. A method of encoding and decoding a digitized image consisting of pixel values, comprising the steps of:
  - detecting sharpness of edges in said digitized image;
  - encoding position and sharpness values of edge points having sharpness values exceeding a certain threshold, thereby generating edge information;
  - filtering said digitized image by a low-pass filter, thereby generating a low-frequency image;
  - encoding said low-frequency image, thereby generating low-frequency information;
  - sending said edge information and said low-frequency information to an input/output device (27);
  - receiving said edge information and said low-frequency information from said input/output device (27);
  - generating a horizontal edge image and a vertical edge image from said edge information;
  - synthesizing a pair of high-frequency images by filtering said horizontal edge image and said vertical edge image with an edge synthesis filter;
  - decoding said low-frequency information, thereby obtaining a decoded low-frequency image; and
  - performing an inverse wavelet transform on said decoded low-frequency image and said pair of high-frequency images.
2. The method of claim 1, wherein detecting sharpness of edges comprises:
  - finding differences between said pixel values; and
  - taking differences of said differences.
3. The method of claim 1, wherein encoding said low-frequency image comprises down-sampling.

4. The method of claim 1, wherein said digitized image comprises successive frames, and encoding position and sharpness values comprises motion estimation.
5. The method of claim 1, wherein said low-pass filter has even symmetry.
- 5 6. The method of claim 1, wherein filtering said digitized image comprises filtering by a series of low-pass filters having different cut-off frequencies, thereby generating a series of images with different resolutions, including at least said digitized image as a highest-resolution image and said low-frequency image as a lowest-resolution image.
- 10 7. The method of claim 6, wherein detecting sharpness of edges comprises:  
detecting sharpness of edges in at least two images among said series of images, excluding said low-frequency image; and  
selecting one of said at least two images for encoding of said position and sharpness values.
- 15 8. The method of claim 6, wherein detecting sharpness of edges comprises:  
selecting one image among said series of images; and  
detecting sharpness of edges in the image thus selected.
- 20 9. The method of claim 1, wherein said edge synthesis filter has coefficients that are generated by filtering a standard edge by a high-pass filter.
10. The method of claim 9, wherein said standard edge consists of a first part and a second part, with pixel values changing at one constant rate in said first part and at a different constant rate in said second part.
- 25 11. The method of claim 9, wherein:  
said step of synthesizing comprises filtering said horizontal edge image with at least two edge synthesis filters and filtering said vertical edge image with said edge synthesis filters, thereby synthesizing at least two pairs of high-frequency images;  
30 said inverse wavelet transform is performed on all said pairs of high-frequency images thus synthesized; and  
said edge synthesis filters are obtained by filtering said standard edge with high-pass filters having different cut-off frequencies.
- 35 12. The method of claim 9, wherein said high-pass filter has even symmetry.
13. The method of claim 9, wherein said high-pass filter has odd symmetry.
- 40 14. The method of claim 13, wherein said step of synthesizing comprises:  
taking differences between consecutive coefficients of said edge synthesis filter, thereby generating a differential edge synthesis filter;  
filtering said horizontal edge image and said vertical edge image by said differential edge synthesis filter, thereby generating a pair of differential high-frequency images; and  
accumulating values in said differential high-frequency images.
- 45 15. A method of encoding a digitized image consisting of pixels having pixel values, comprising the steps of:  
filtering said digitized image by a low-pass filter, thereby generating a low-frequency image;  
down-sampling said low-frequency image, thereby generating a reduced image smaller in size than said digitized image;  
50 encoding said reduced image, thereby generating low-frequency information;  
generating an edge image by detecting edges in said digitized image;  
encoding said edge image, thereby generating edge information; and  
sending said edge information and said low-frequency information to an input/output device (27).
- 55 16. The method of claim 15, wherein said low-pass filter has even symmetry.
17. The method of claim 15, wherein said low-pass filter satisfies an exact reconstruction condition.

18. The method of claim 15, wherein detecting edges comprises detecting variations in rate of change of said pixel values from pixel to pixel.
19. The method of claim 15, wherein generating an edge image comprises:  
 5       detecting edges in a horizontal direction, thereby generating a horizontal edge image; and  
       detecting edges in a vertical direction, thereby generating a vertical edge image.
20. The method of claim 15, said method being applied to successive frames of a moving image, wherein the step of encoding said edge image comprises:  
 10       reading a previous edge image, pertaining to a previous frame, from a first memory (72);  
       generating edge motion vectors by detecting motion of said edge image with respect to said previous edge image;  
       encoding said edge motion vectors, thereby generating edge motion information as part of said edge information;  
 15       generating an edge difference image by detecting differences between said edge image and said previous edge image, over and above said motion of said edge image; and  
       encoding said edge difference image, thereby generating edge difference information as another part of said edge information.
21. The method of claim 20, wherein encoding said edge image comprises the further steps of:  
 20       decoding said edge difference information, thereby generating a decoded edge difference image;  
       adding said decoded edge difference image to said previous edge image to generate a new edge image; and  
       storing said new edge image in said first memory (72), for use as a previous edge image in a subsequent frame.  
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22. The method of claim 20, wherein the step of encoding said reduced image comprises:  
       reading a previous reduced image, pertaining to said previous frame, from a second memory (76);  
       generating low-frequency motion vectors by detecting motion of said reduced image with respect  
 30       to said previous reduced image;  
       encoding said low-frequency motion vectors, thereby generating low-frequency motion information as part of said low-frequency information;  
       generating a reduced difference image by detecting differences between said reduced image and said previous reduced image, over and above said motion of said reduced image; and  
 35       encoding said reduced difference image, thereby generating low-frequency difference information as another part of said low-frequency information.
23. The method of claim 22, wherein encoding said reduced image comprises the further steps of:  
 40       decoding said low-frequency difference information, thereby generating a decoded reduced difference image;  
       adding said decoded reduced difference image to said previous reduced image to generate a new reduced image; and  
       storing said new reduced image in said second memory (76), for use as a previous reduced image in a subsequent frame.  
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24. The method of claim 22, wherein generating edge motion vectors comprises using said low-frequency motion vectors to select a range of search for said edge motion vectors.
25. The method of claim 22, wherein generating low-frequency motion vectors comprises using said edge motion vectors to select a range of search for said low-frequency motion vectors.  
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26. A method of reconstructing a digitized image from encoded edge information and low-frequency information, comprising the steps of:  
 55       generating a horizontal edge image and a vertical edge image by decoding said edge information;  
       synthesizing a pair of high-frequency images by filtering said horizontal edge image horizontally with an edge synthesis filter, and filtering said vertical edge image vertically with said edge synthesis filter;  
       generating a reduced image by decoding said low-frequency information;  
       up-sampling said reduced image, thereby generating a low-frequency image equal in size to said

high-frequency images;

performing an inverse wavelet transform on said high-frequency images and said low-frequency image, thereby obtaining said digitized image; and

sending said digitized image to an input/output device (37).

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27. The method of claim 26, wherein said edge synthesis filter has coefficients that are generated by filtering a standard edge, having just one sharp variation, by a high-pass filter.

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28. The method of claim 27, wherein said standard edge consists of a first part and a second part, with pixel values changing at one constant rate in said first part and at a different constant rate in said second part.

29. The method of claim 27, wherein the step of performing an inverse wavelet transform comprises filtering said high-frequency images by a conjugate filter of said high-pass filter.

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30. The method of claim 27, wherein said high-pass filter satisfies an exact reconstruction condition.

31. The method of claim 27, wherein said high-pass filter has even symmetry.

32. The method of claim 27, wherein said high-pass filter has odd symmetry.

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33. The method of claim 32, wherein filtering with said edge synthesis filter is performed by using a differential edge synthesis filter having coefficients equal to differences between consecutive coefficients of said edge synthesis filter, and said step of synthesizing a pair of high-frequency images comprises:

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filtering said horizontal edge image horizontally by said differential edge synthesis filter, thereby synthesizing a horizontal differential high-frequency image;

accumulating values in said horizontal differential high-frequency image;

filtering said vertical edge image vertically by said differential edge synthesis filter, thereby synthesizing a vertical differential high-frequency image; and

accumulating values in said vertical differential high-frequency image.

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34. The method of claim 26, said method being applied to successive frames of an encoded moving image, wherein said edge information comprises edge difference information and edge motion information, and decoding said edge information comprises:

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decoding said edge difference information to obtain an edge difference image;

decoding said edge motion information to obtain edge motion vectors;

reading a previous edge image, pertaining to a previous frame, from a third memory (85);

adding said edge difference image to said previous edge image according to said edge motion vectors, to obtain a new edge image; and

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storing said new edge image in said third memory (85) for use as a previous edge image of a subsequent frame.

35. The method of claim 34, wherein said low-frequency information comprises low-frequency difference information and low-frequency motion information, and decoding said low-frequency information comprises:

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decoding said low-frequency difference information to obtain a reduced difference image;

decoding said low-frequency motion information to obtain low-frequency motion vectors;

reading a previous reduced image, pertaining to said previous frame, from a fourth memory (87);

adding said reduced difference image to said previous reduced image according to said low-frequency motion vectors, to obtain said reduced image; and

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storing said reduced image in said fourth memory (87) for use as a previous reduced image of a subsequent frame.

36. A method of encoding a digitized image consisting of pixels having pixel values, comprising the steps of: filtering said digitized image by a series of low-pass filters having progressively lower cut-off frequencies, thereby generating a series of filtered images culminating in a low-frequency image, which was filtered with a lowest cut-off frequency among said cut-off frequencies;

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down-sampling said low-frequency image, thereby generating a reduced image smaller in size than said digitized image;

encoding said reduced image, thereby generating low-frequency information;  
 generating an edge image by detecting edges in one image among said digitized image and said  
 filtered images, excluding said low-frequency image;  
 encoding said edge image, thereby generating edge information; and  
 sending said edge information and said low-frequency information to an input/output device (27).

37. The method of claim 36, wherein said low-pass filters have even symmetry.

38. The method of claim 36, wherein detecting edges comprises detecting variations in rate of change of in-  
 tensity from pixel to pixel.

39. The method of claim 36, wherein generating an edge image comprises:  
 detecting edges in a horizontal direction, thereby generating a horizontal edge image; and  
 detecting edges in a vertical direction, thereby generating a vertical edge image.

40. The method of claim 36, wherein generating an edge image comprises:  
 generating a plurality of edge images by detecting edges in a plurality of images among said digi-  
 tized image and said filtered images, excluding said low-frequency image; and  
 selecting one of said plurality of edge images.

41. The method of claim 36, wherein generating an edge image comprises:  
 selecting one image among said digitized image and said filtered images, excluding said low-fre-  
 quency image; and  
 detecting edges in said one image.

42. The method of claim 36, said method being applied to successive frames of a moving image, wherein the  
 step of encoding said edge image comprises:  
 reading a previous edge image, pertaining to a previous frame, from a first memory (72);  
 generating edge motion vectors by detecting motion of said edge image with respect to said pre-  
 vious edge image;  
 encoding said edge motion vectors, thereby generating edge motion information as part of said  
 edge information;  
 generating an edge difference image by detecting differences between said edge image and said  
 previous edge image, over and above said motion of said edge image; and  
 encoding said edge difference image, thereby generating edge difference information as another  
 part of said edge information.

43. The method of claim 42, wherein encoding said edge image comprises the further steps of:  
 decoding said edge difference information, thereby generating a decoded edge difference image;  
 adding said decoded edge difference image to said previous edge image to generate a new edge  
 image; and  
 storing said new edge image in said first memory (72), for use as a previous edge image in a sub-  
 sequent frame.

44. The method of claim 42, wherein the step of encoding said reduced image comprises:  
 reading a previous reduced image, pertaining to said previous frame, from a second memory (76);  
 generating low-frequency motion vectors by detecting motion of said reduced image with respect  
 to said previous reduced image;  
 encoding said low-frequency motion vectors, thereby generating low-frequency motion information  
 as part of said low-frequency information;  
 generating a reduced difference image by detecting differences between said reduced image and  
 said previous reduced image, over and above said motion of said reduced image; and  
 encoding said reduced difference image, thereby generating low-frequency difference information  
 as another part of said low-frequency information.

45. The method of claim 44, wherein encoding said reduced image comprises the further steps of:  
 decoding said low-frequency difference information, thereby generating a decoded reduced dif-  
 ference image;

adding said decoded reduced difference image to said previous reduced image to generate a new reduced image; and

storing said new reduced image in said second memory (76), for use as a previous reduced image in a subsequent frame.

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46. The method of claim 44, wherein generating edge motion vectors comprises using said low-frequency motion vectors to select a range of search for said edge motion vectors.

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47. The method of claim 44, wherein generating low-frequency motion vectors comprises using said edge motion vectors to select a range of search for said low-frequency motion vectors.

48. A method of reconstructing a digitized image from encoded edge information and low-frequency information, comprising the steps of:

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generating a horizontal edge image and a vertical edge image by decoding said edge information;

synthesizing a series of pairs of high-frequency images by filtering said horizontal edge image horizontally with a series of edge synthesis filters, and filtering said vertical edge image vertically with said series of edge synthesis filters;

generating a reduced image by decoding said low-frequency information;

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up-sampling said reduced image, thereby generating a low-frequency image equal in size to said high-frequency images;

performing an inverse wavelet transform on said series of pairs of high-frequency images and said low-frequency image, thereby obtaining said digitized image; and

sending said digitized image to an input/output device (37).

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49. The method of claim 48, wherein said series of edge synthesis filters is generated by filtering a standard edge, having just one sharp variation, by a corresponding series of high-pass filters with progressively decreasing cut-off frequencies.

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50. The method of claim 49, wherein said standard edge consists of a first part and a second part, with pixel values changing at one constant rate in said first part and at a different constant rate in said second part.

51. The method of claim 49, wherein the step of performing an inverse wavelet transform comprises filtering said pairs of high-frequency images by respective conjugate filters of said series of high-pass filters.

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52. The method of claim 49, wherein said high-pass filters have even symmetry.

53. The method of claim 49, wherein said high-pass filters have odd symmetry.

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54. The method of claim 53, wherein filtering said pair of edge images by said edge synthesis filters comprises:

taking differences between consecutive coefficients of said edge synthesis filters, thereby generating a series of differential edge synthesis filters;

filtering said horizontal edge image horizontally by said differential edge synthesis filters, thereby synthesizing a series of differential high-frequency images;

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accumulating values in respective horizontal differential high-frequency images;

filtering said vertical edge image vertically by said differential edge synthesis filters, thereby synthesizing a series of differential high-frequency images; and

accumulating values in respective vertical differential high-frequency images.

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55. The method of claim 48, said method being applied to successive frames of an encoded moving image, wherein said edge information comprises edge difference information and edge motion information, and decoding said edge information comprises:

decoding said edge difference information to obtain an edge difference image;

decoding said edge motion information to obtain edge motion vectors;

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reading a previous edge image, pertaining to a previous frame, from a third memory (85);

adding said edge difference image to said previous edge image according to said edge motion vectors, to obtain a new edge image; and

storing said new edge image in said third memory (85) for use as a previous edge image of a sub-

sequent frame.

56. The method of claim 55, wherein said low-frequency information comprises low-frequency difference information and low-frequency motion information, and decoding said low-frequency information comprises:
  - decoding said low-frequency difference information to obtain a reduced difference image;
  - decoding said low-frequency motion information to obtain low-frequency motion vectors;
  - reading a previous reduced image, pertaining to said previous frame, from a fourth memory (87);
  - adding said reduced difference image to said previous reduced image according to said low-frequency motion vectors, to obtain said reduced image; and
  - storing said reduced image in said fourth memory (87) for use as a previous reduced image of a subsequent frame.
57. A digital image encoder for encoding a digitized image consisting of pixels having pixel values, comprising:
  - an input terminal (10) for receiving said digitized image;
  - a smoothing filter (20) coupled to said input terminal (10), for filtering said digitized image to generate a low-frequency image;
  - a down sampler (23) coupled to said smoothing filter (20), for down-sampling said low-frequency image to generate a reduced image;
  - an edge detector (21) coupled to said input terminal (10), for generating an edge image by detecting edge points in said digitized image; and
  - an encoding section (22) coupled to said edge detector (21) and said down sampler (23), for encoding said edge image and said reduced image.
58. The digital image encoder of claim 57, wherein said edge detector (21) detects variations in rate of change of said pixel values from pixel to pixel.
59. The digital image encoder of claim 57, wherein said smoothing filter (20) is a low-pass filter with even symmetry.
60. The digital image encoder of claim 58, wherein said low-pass filter satisfies an exact reconstruction condition.
61. A digital image encoder for encoding a digitized image consisting of pixels having pixel values, comprising:
  - an input terminal (10) for receiving said digitized image;
  - a series of smoothing filters (20-m) coupled to said input terminal (10), having progressively lower cut-off frequencies, for filtering said digitized image to generate a series of filtered images culminating in a low-frequency image, which was filtered with a lowest cut-off frequency among said cut-off frequencies;
  - a down sampler (23) coupled to a last one of said smoothing filters (20-n) in said series, for down-sampling said low-frequency image to generate a reduced image;
  - a plurality of edge detectors (21-m) for generating respective edge images by detecting edge points in a plurality of images among said digitized image and said filtered images, excluding said low-frequency image;
  - a selector (50) coupled to said plurality of edge detectors (21-m), for selecting an edge image from among said edge images; and
  - an encoding section coupled to said selector (50) and said down sampler (23), for encoding said edge image and said reduced image.
62. The digital image encoder of claim 61, wherein said smoothing filters (20-m) are coupled in cascade to said input terminal (10).
63. The digital image encoder of claim 61, wherein said smoothing filters (20-m) are low-pass filters with even symmetry.
64. The digital image encoder of claim 61, wherein said edge detectors (21-m) detect variations in rate of change of said pixel values from pixel to pixel.

65. The digital image encoder of claim 61, wherein said encoding section (22) comprises:  
 an edge image encoder (24) coupled to said selector (50), for encoding said edge image to generate edge information;  
 a low-frequency image encoder (25) coupled to said down sampler (23), for encoding said reduced image to generate low-frequency information; and  
 a multiplexer (26), coupled to said edge image encoder (24) and said low-frequency image encoder (25), for multiplexing said low-frequency information and said edge information.
66. The digital image encoder of claim 61, wherein said digitized image is a moving image consisting of successive frames, and said encoding section (65) comprises:  
 a first memory (72) for storing a previous edge image, pertaining to a previous frame among said frames;  
 a first motion estimator (66) coupled to said selector (50) and said first memory (72), for detecting motion of said edge image with respect to said previous edge image, generating edge motion vectors, detecting differences between said edge image and said previous edge image over and above said motion of said edge image, and generating an edge difference image;  
 a second memory (76) for storing a previous reduced image, pertaining to said previous frame; and  
 a second motion estimator (67) coupled to said down sampler (23) and said second memory (76), for detecting motion of said reduced image with respect to said previous reduced image, generating low-frequency motion vectors, detecting differences between said reduced image and said previous reduced image over and above said motion of said reduced image, and generating a reduced difference image.
67. The digital image encoder of claim 66, wherein:  
 said first motion estimator (66) is coupled to said second motion estimator (67); and  
 said first motion estimator (66) uses said low-frequency motion vectors to select a range of search for said edge motion vectors.
68. The digital image encoder of claim 66, wherein:  
 said first motion estimator (66) is coupled to said second motion estimator (67); and  
 said second motion estimator (67) uses said edge motion vectors to select a range of search for said low-frequency motion vectors.
69. The digital image encoder of claim 66, wherein said encoding section (65) also comprises:  
 a first difference encoder (69) coupled to said first motion estimator (66), for encoding said edge difference image, thereby generating edge difference information;  
 a first difference decoder (70) coupled to said first difference encoder (69), for decoding said edge difference information, thereby generating a decoded edge difference image;  
 a first adder (71) for adding said decoded edge difference image to said previous edge image to generate a new edge image, and storing said new edge image in said first memory (72) for use as a previous edge image in a subsequent frame among said frames;  
 a second difference encoder (73) coupled to said second motion estimator (67), for encoding said reduced difference image, thereby generating low-frequency difference information;  
 a second difference decoder (74) coupled to said second difference encoder (73), for decoding said low-frequency difference information, thereby generating a decoded reduced difference image;  
 a second adder (75) for adding said decoded reduced difference image to said previous reduced image to generate a new reduced difference image, and storing said new reduced difference image in said second memory (76) for use as a previous reduced image in said subsequent frame;  
 a motion vector encoder (68) coupled to said first motion estimator (66) and said second motion estimator (67), for encoding said edge motion vectors and said low-frequency motion vectors to generate motion information; and  
 a multiplexer (78) coupled to said motion vector encoder (68), said first difference encoder (69), and said second difference encoder (73), for multiplexing said motion information, said edge difference information, and said low-frequency information.
70. A digital image encoder for encoding a digitized image consisting of pixels having pixel values, comprising:  
 an input terminal (10) for receiving said digitized image;  
 a series of smoothing filters (20-m) coupled to said input terminal (10), having progressively lower

cut-off frequencies, for filtering said digitized image to generate a series of filtered images culminating in a low-frequency image, which was filtered with a lowest cut-off frequency among said cut-off frequencies;

a down sampler (23) coupled to a last one of said smoothing filters (20-n) in said series, for down-sampling said low-frequency image to generate a reduced image;

a selector (52) coupled to said series of smoothing filters (20-m), for selecting an image from among said digitized image and said filtered images, excluding said low-frequency image;

an edge detector (21) coupled to said selector (52), for generating an edge image by detecting edge points in the image selected by said selector (52); and

an encoding section coupled to said down sampler (23) and said edge detector (21), for encoding said edge image and said reduced image.

71. The digital image encoder of claim 70, wherein said smoothing filters (20-m) are coupled in cascade to said input terminal (10).

72. The digital image encoder of claim 70, wherein said smoothing filters (20-m) are low-pass filters with even symmetry.

73. The digital image encoder of claim 70, wherein said edge detector (21) detects variations in rate of change of said pixel values from pixel to pixel.

74. The digital image encoder of claim 70, wherein said encoding section (22) comprises:

an edge image encoder (24) coupled to said edge detector (21), for encoding said edge image to generate edge information;

a low-frequency image encoder (25) coupled to said down sampler (23), for encoding said reduced image to generate low-frequency information; and

a multiplexer (26), coupled to said edge image encoder (24) and said low-frequency image encoder (25), for multiplexing said low-frequency information and said edge information.

75. The digital image encoder of claim 70, wherein said digitized image is a moving image consisting of successive frames, and said encoding section (65) comprises:

a first memory (72) for storing a previous edge image, pertaining to a previous frame among said frames;

a first motion estimator (66) coupled to said selector (50) and said first memory (72), for detecting motion of said edge image with respect to said previous edge image, generating edge motion vectors, detecting differences between said edge image and said previous edge image over and above said motion of said edge image, and generating an edge difference image;

a second memory (76) for storing a previous reduced image, pertaining to said previous frame; and

a second motion estimator (67) coupled to said down sampler (23) and said second memory (76), for detecting motion of said reduced image with respect to said previous reduced image, generating low-frequency motion vectors, detecting differences between said reduced image and said previous reduced image over and above said motion of said reduced image, and generating a reduced difference image.

76. The digital image encoder of claim 75, wherein:

said first motion estimator (66) is coupled to said second motion estimator (67); and

said first motion estimator (66) uses said low-frequency motion vectors to select a range of search for said edge motion vectors.

77. The digital image encoder of claim 75, wherein:

said first motion estimator (66) is coupled to said second motion estimator (67); and

said second motion estimator (67) uses said edge motion vectors to select a range of search for said low-frequency motion vectors.

78. The digital image encoder of claim 75, wherein said encoding section (22) also comprises:

a first difference encoder (69) coupled to said first motion estimator (66), for encoding said edge difference image, thereby generating edge difference information;

a first difference decoder (70) coupled to said first difference encoder (69), for decoding said edge

difference information, thereby generating a decoded edge difference image;

a first adder (71) for adding said decoded edge difference image to said previous edge image to generate a new edge image, and storing said new edge image in said first memory (72) for use as a previous edge image in a subsequent frame among said frames;

a second difference encoder (73) coupled to said second motion estimator (67), for encoding said reduced difference image, thereby generating low-frequency difference information;

a second difference decoder (74) coupled to said second difference encoder (73), for decoding said low-frequency difference information, thereby generating a decoded reduced difference image;

an adder (75) for adding said decoded reduced difference image to said previous reduced image to generate a new reduced difference image, and storing said new reduced difference image in said second memory (76) for use as a previous reduced image in said subsequent frame;

a motion vector encoder (68) coupled to said first motion estimator (66) and said second motion estimator (67), for encoding said edge motion vectors and said low-frequency motion vectors to generate motion information; and

a multiplexer (78) coupled to said motion vector encoder (68), said first difference encoder (69), and said second difference encoder (73), for multiplexing said motion information, said edge difference information, and said low-frequency information.

79. A digital image decoder for reconstructing a digitized image from encoded edge information and low-frequency information, comprising:

a decoding section (28) for decoding said edge information to generate a horizontal and vertical pair of edge images, and decoding said low-frequency information to generate a reduced image;

an up sampler (35) coupled to said decoding section (28), for up-sampling said reduced image to generate a low-frequency image equal in size to said edge images;

an edge synthesizer (34) coupled to said decoding section (28), for synthesizing a pair of high-frequency images by filtering said pair of edge images with an edge synthesis filter; and

an inverse wavelet transform processor (36), coupled to said up sampler (35) and said edge synthesizer (34), for performing an inverse wavelet transform on said decoded low-frequency image and said pair of high-frequency images, thereby obtaining said digitized image.

80. The digital image decoder of claim 79, wherein said edge synthesis filter has coefficients that are generated by using a high-pass filter to filter a standard edge.

81. The digital image decoder of claim 80, wherein said standard edge consists of a first part and a second part, with pixel values that vary at one constant rate in said first part and at a different constant rate in said second part.

82. The digital image decoder of claim 80, wherein said high-pass filter satisfies a complete reconstruction condition.

83. The digital image decoder of claim 80, wherein said inverse wavelet transform processor (36) filters said pair of high-frequency images by a conjugate of said high-pass filter.

84. The digital image decoder of claim 80, wherein said high-pass filter has even symmetry.

85. The digital image decoder of claim 80, wherein said high-pass filter has odd symmetry.

86. The digital image decoder of claim 85, wherein said edge synthesizer (34) comprises:

a pair of differential edge synthesizers (44, 45) for filtering respective edge images by a filter having coefficients equal to differences between consecutive coefficients of said edge synthesis filter, thereby generating a pair of differential high-frequency image; and

a pair of accumulators (46, 47) for accumulating values in respective differential high-frequency images.

87. A digital image decoder for reconstructing a digitized image from an encoded image consisting of edge information and low-frequency information, comprising:

a decoding section for decoding said edge information to generate a pair of edge images, comprising a horizontal edge image and a vertical edge image, and decoding said low-frequency information to

generate a reduced image;

an up sampler (35), coupled to said decoding section, for up-sampling said reduced image to generate a decoded low-frequency image equal in size to said edge images;

5 a plurality of edge synthesizers (34-m), each coupled to said edge information decoder (31), having respective edge synthesis filters, for synthesizing a series of pairs of high-frequency images by filtering said horizontal edge image horizontally and said vertical edge image vertically with said edge synthesis filters; and

10 an inverse wavelet transform processor (36), coupled to said up sampler (35) and said plurality of edge synthesizers (34-m), for performing an inverse wavelet transform on said decoded low-frequency image and said series of pairs of high-frequency images.

88. The digital image decoder of claim 87, wherein said edge synthesis filters have coefficients generated by filtering a standard edge with respective high-pass filters having progressively decreasing cut-off frequencies.

15 89. The digital image decoder of claim 88, wherein said standard edge consists of a first part and a second part, with pixel values that vary at one constant rate in said first part and at a different constant rate in said second part.

20 90. The digital image decoder of claim 88, wherein said inverse wavelet transform processor (36) filters said pairs of high-frequency images by conjugate filters of respective high-pass filters.

91. The digital image decoder of claim 88, wherein said high-pass filters have even symmetry.

25 92. The digital image decoder of claim 88, wherein said high-pass filters have odd symmetry.

93. The digital image decoder of claim 92, wherein each of said edge synthesizers (34-m) separately comprises:

30 a pair of differential edge synthesizers (44, 45) for filtering said horizontal edge image horizontally and said vertical edge image vertically by a filter having coefficients equal to differences between consecutive coefficients of said edge synthesis filter, thereby generating a pair of differential high-frequency images; and

35 a pair of accumulators (46, 47) for accumulating values in respective differential high-frequency images.

94. The digital image decoder of claim 87, wherein said decoding section (28) comprises:

an edge information decoder (31), for generating said pair of edge images by decoding said edge information; and

40 a low-frequency information decoder (32), for generating said reduced image by decoding said low-frequency information.

95. The digital image decoder of claim 87, wherein:

said encoded image is an encoded moving image comprising successive frames;

said edge information comprises edge motion information and edge difference information;

45 said low-frequency information comprises low-frequency motion information and low-frequency difference information; and

said decoding section (79) performs motion compensation.

96. The digital image decoder of claim 95, wherein said decoding section (79) comprises:

50 a third difference decoder (81) for decoding said edge difference information to obtain an edge difference image;

a motion information decoder (82) for decoding said edge motion information and said low-frequency motion information to obtain edge motion vectors and low-frequency motion vectors;

55 a fourth difference decoder (83) for decoding said low-frequency difference information to obtain a reduced difference image;

a third memory (85) for storing a previous edge image, pertaining to a previous frame among said frames;

a first motion compensator (84) coupled to said third difference decoder (81), said motion infor-

mation decoder (82), and said third memory (85), for adding said edge difference image to said previous edge image according to said edge motion vectors to generate a new edge image, storing said new edge image in said third memory (85) for use as a previous edge image in a subsequent frame among said frames, and generating said pair of edge images;

5       a fourth memory (87) for storing a previous reduced image pertaining to said previous frame; and  
      a second motion compensator (86) coupled to said motion information decoder (82), said fourth difference decoder (83), and said fourth memory (87), for adding said reduced difference image to said previous reduced image according to said low-frequency motion vectors, thereby generating said reduced image, and storing said reduced image in said fourth memory (87) for use as a previous reduced image  
10       in said subsequent frame.

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FIG. 1

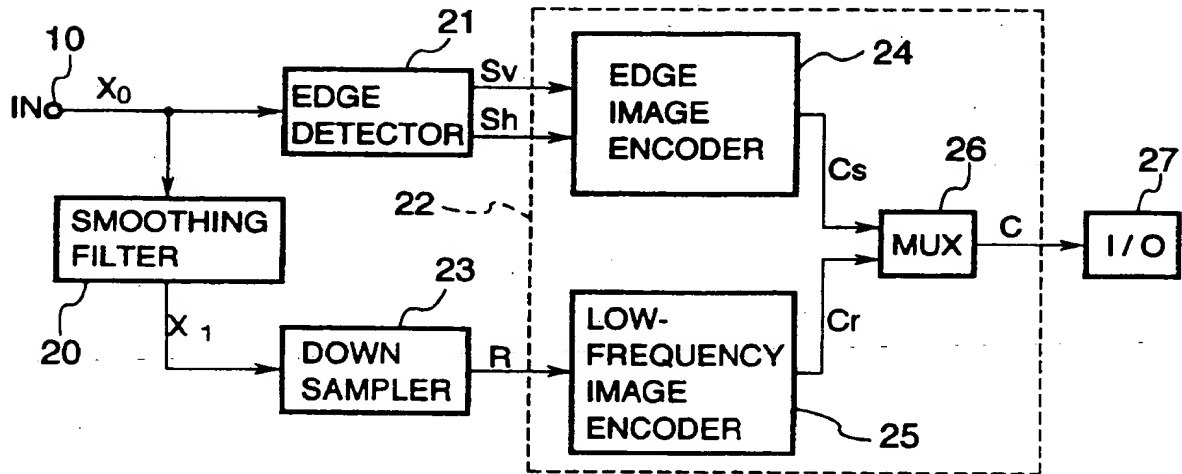


FIG. 2

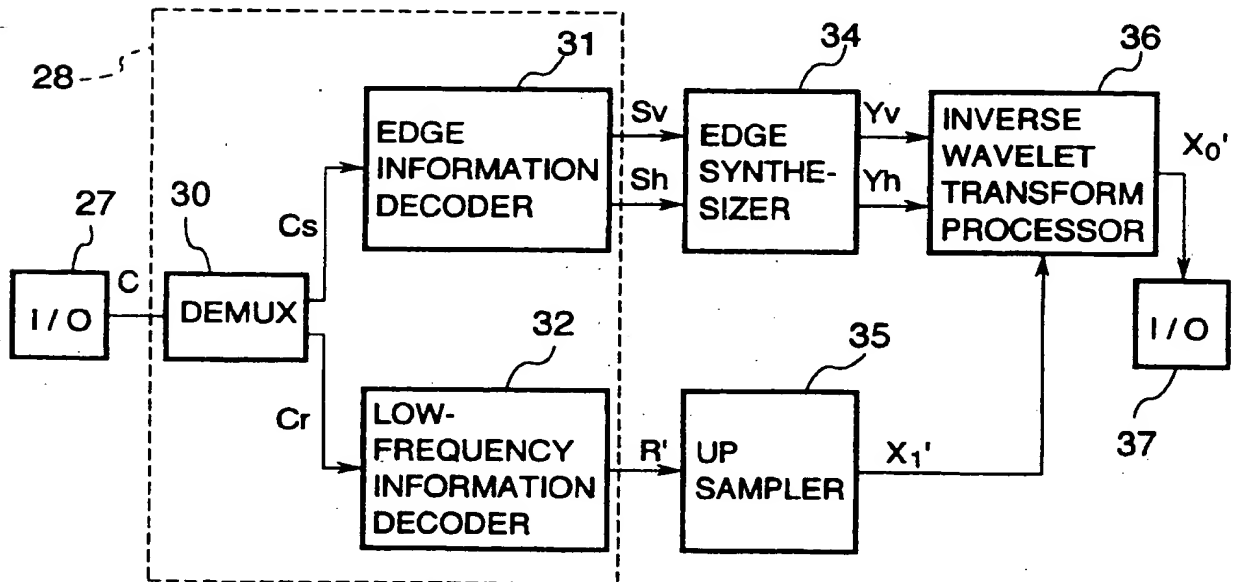


FIG. 3

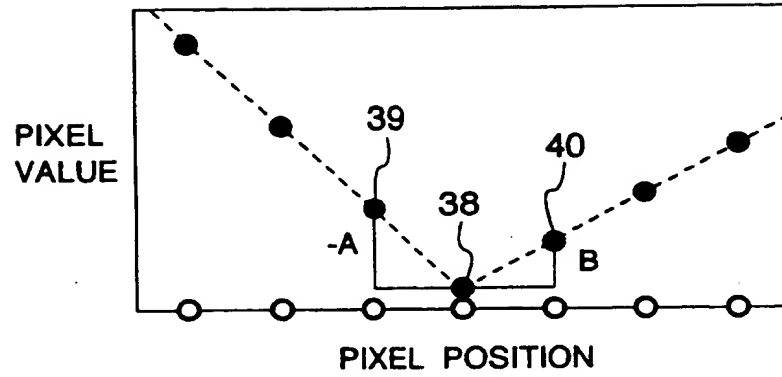


FIG. 4

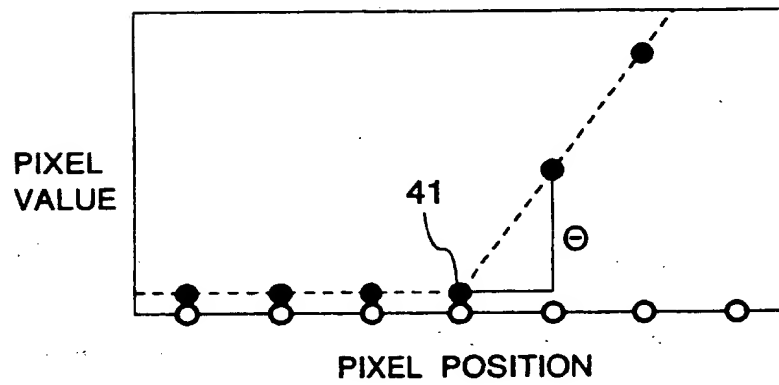


FIG. 5

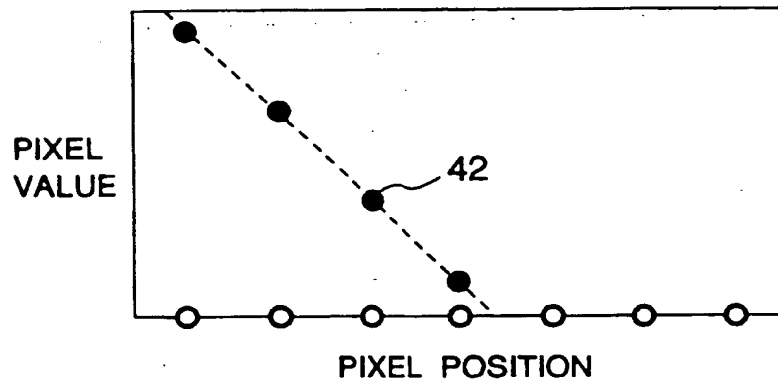


FIG. 6

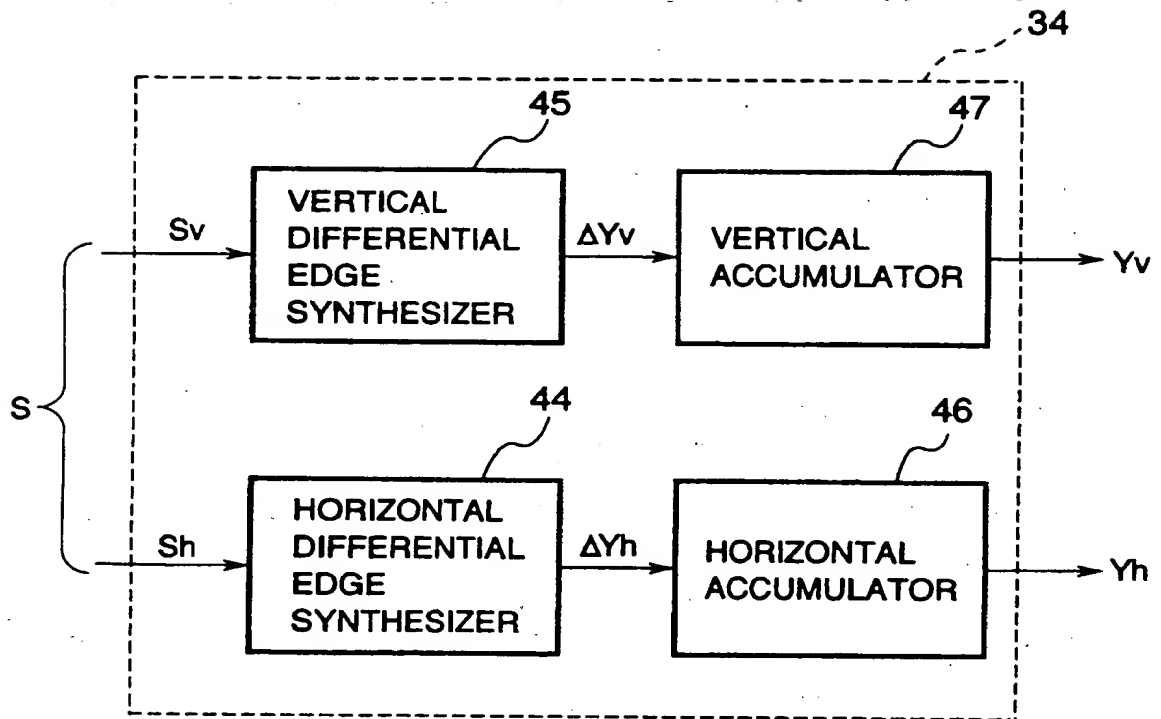


FIG. 7

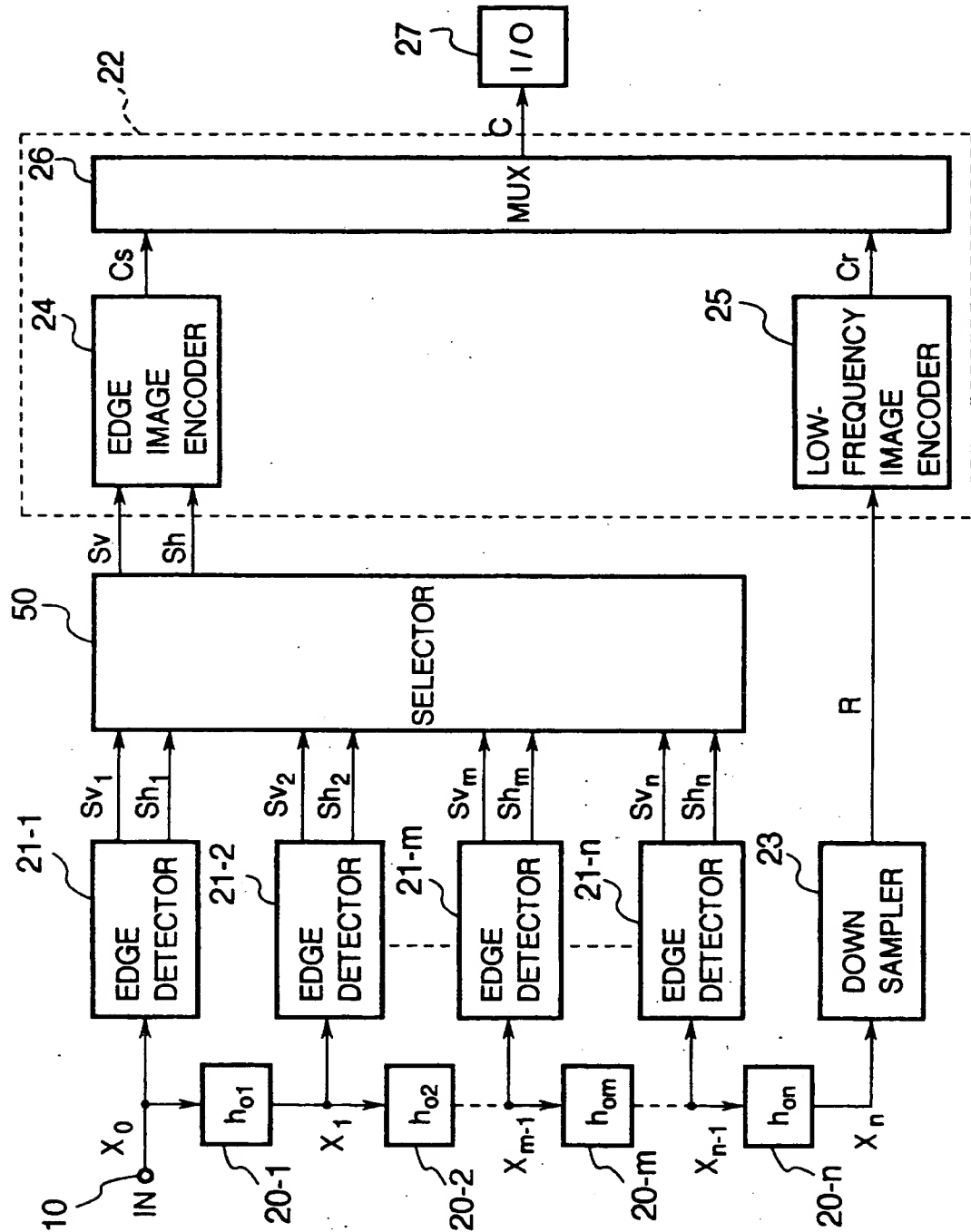


FIG. 8

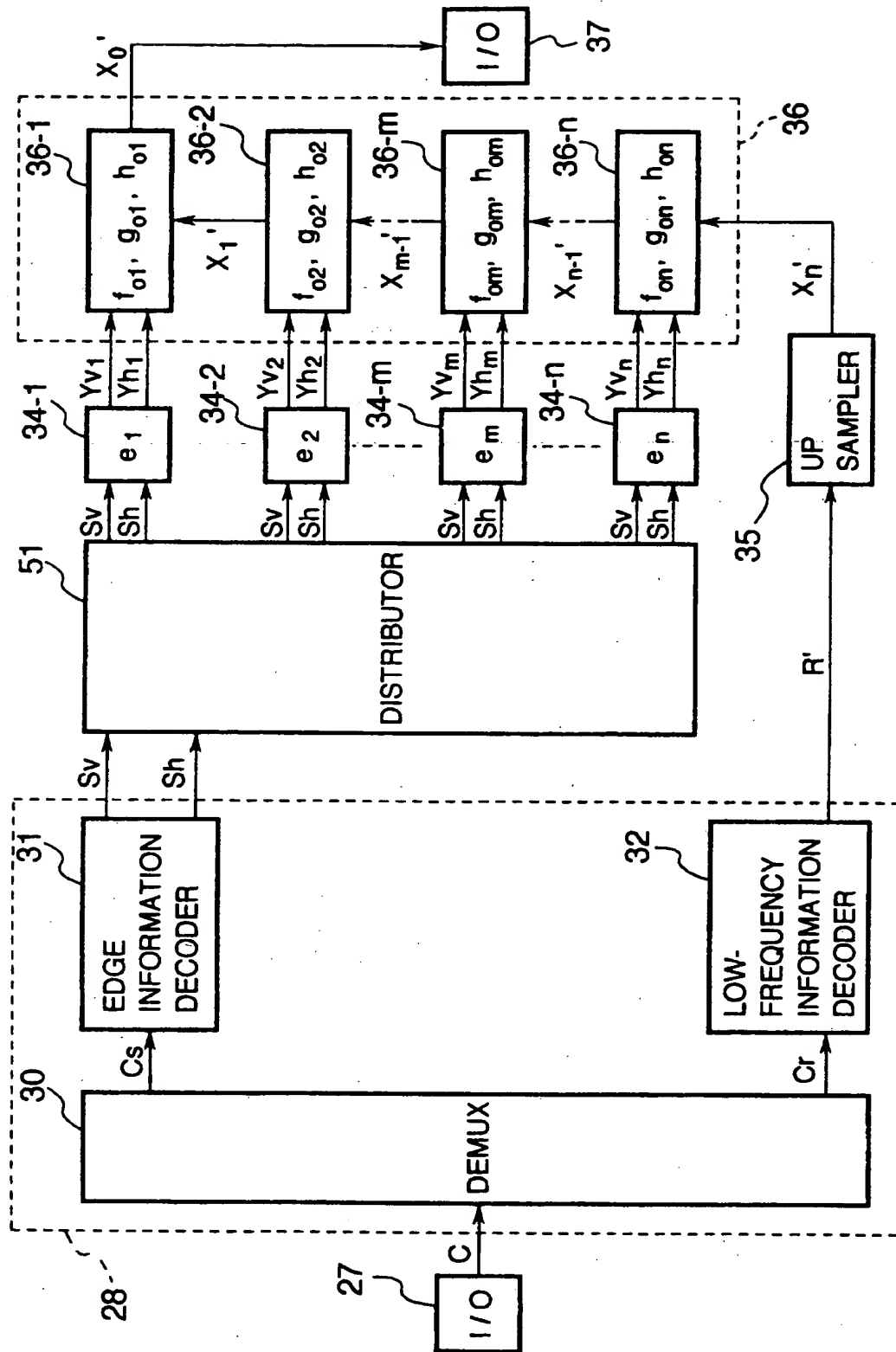


FIG. 9

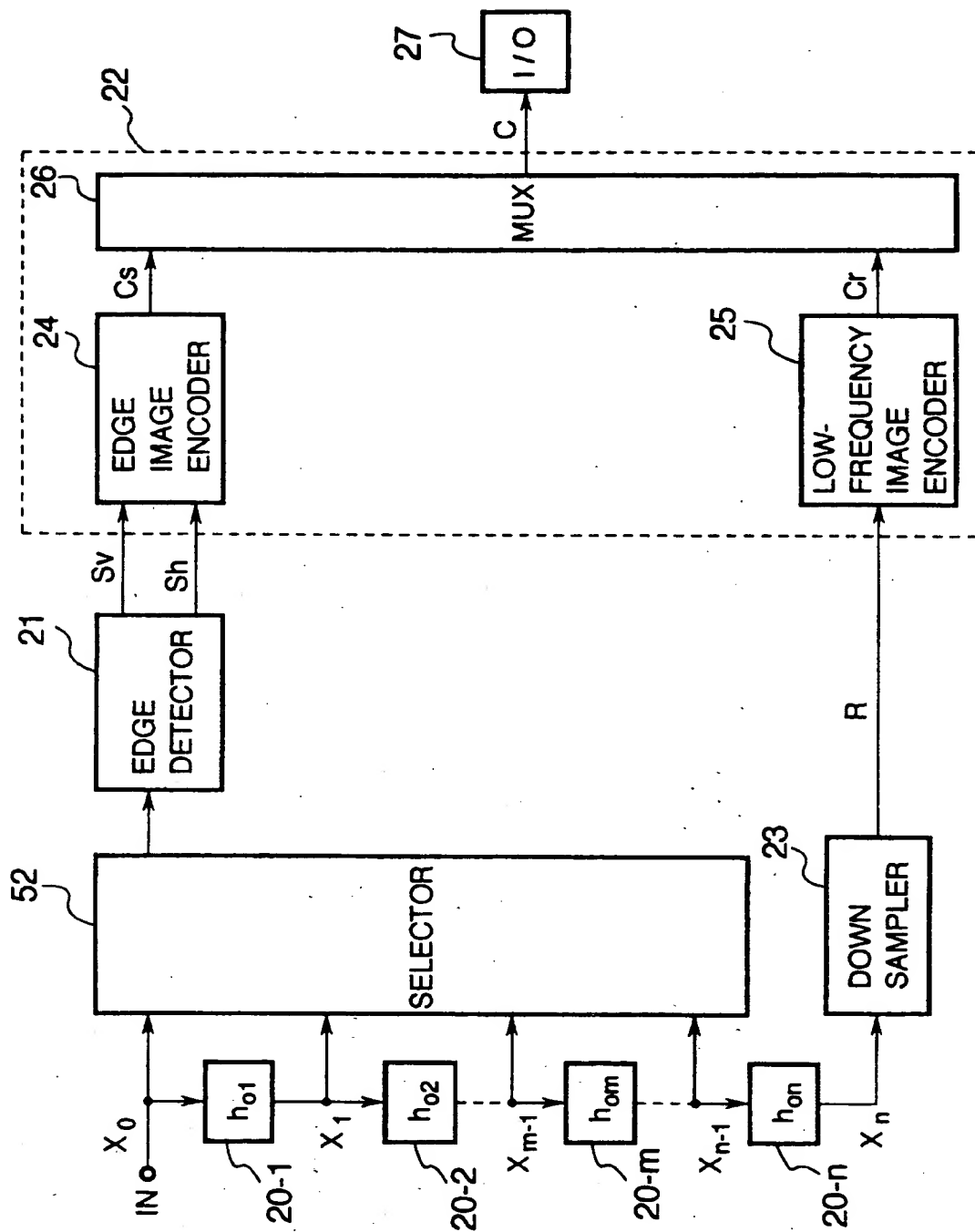


FIG. 10

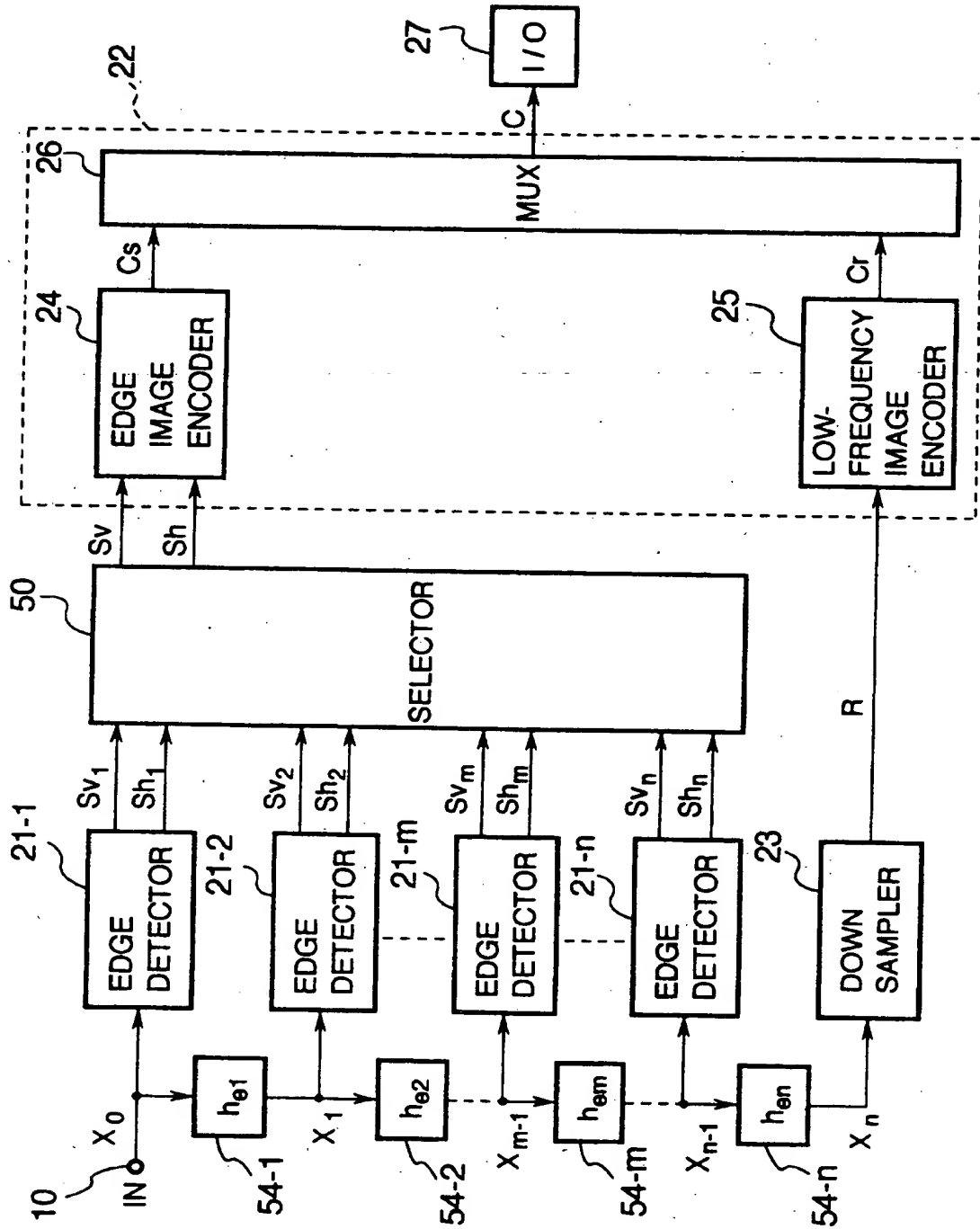


FIG. 11

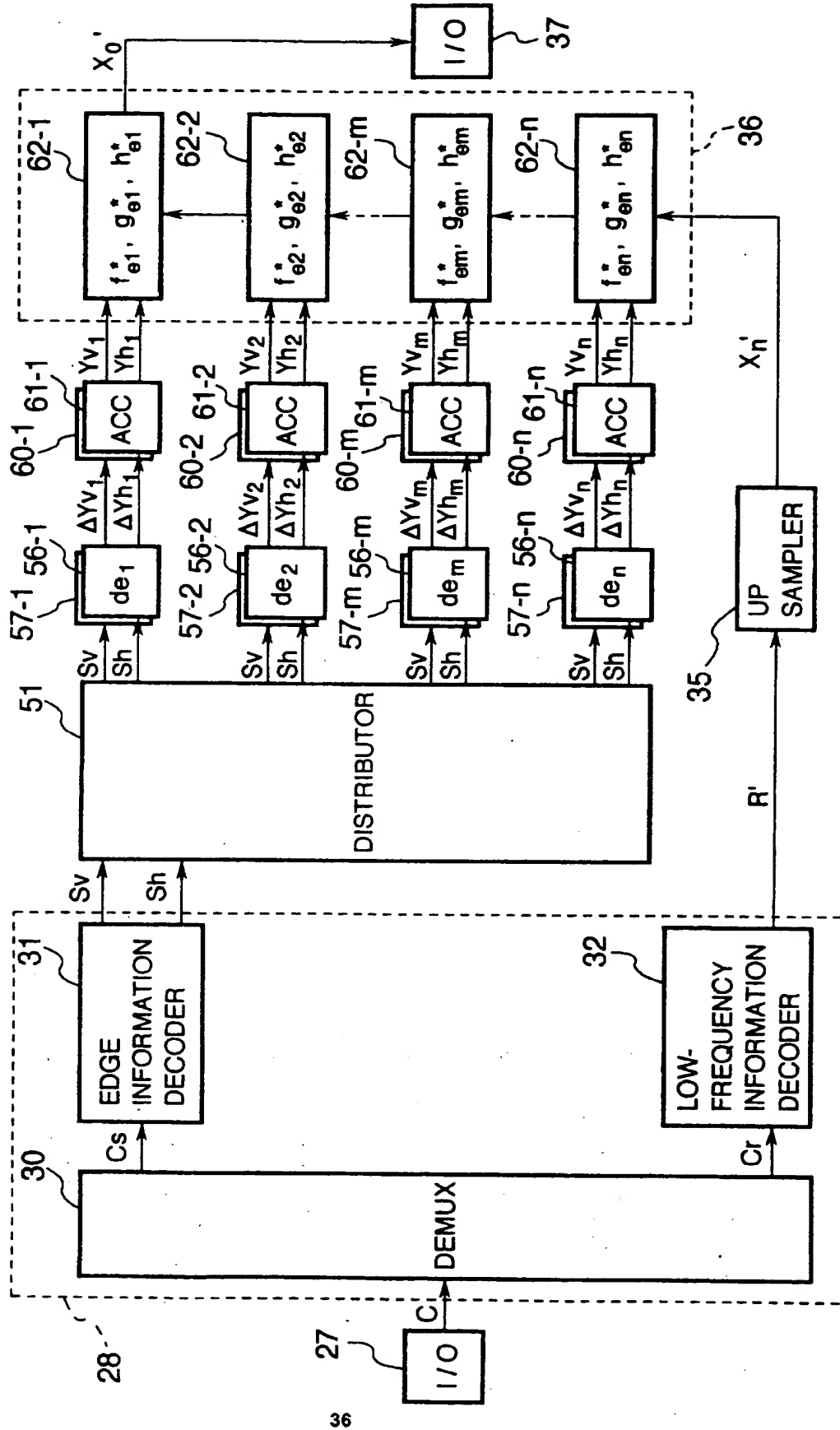


FIG. 12

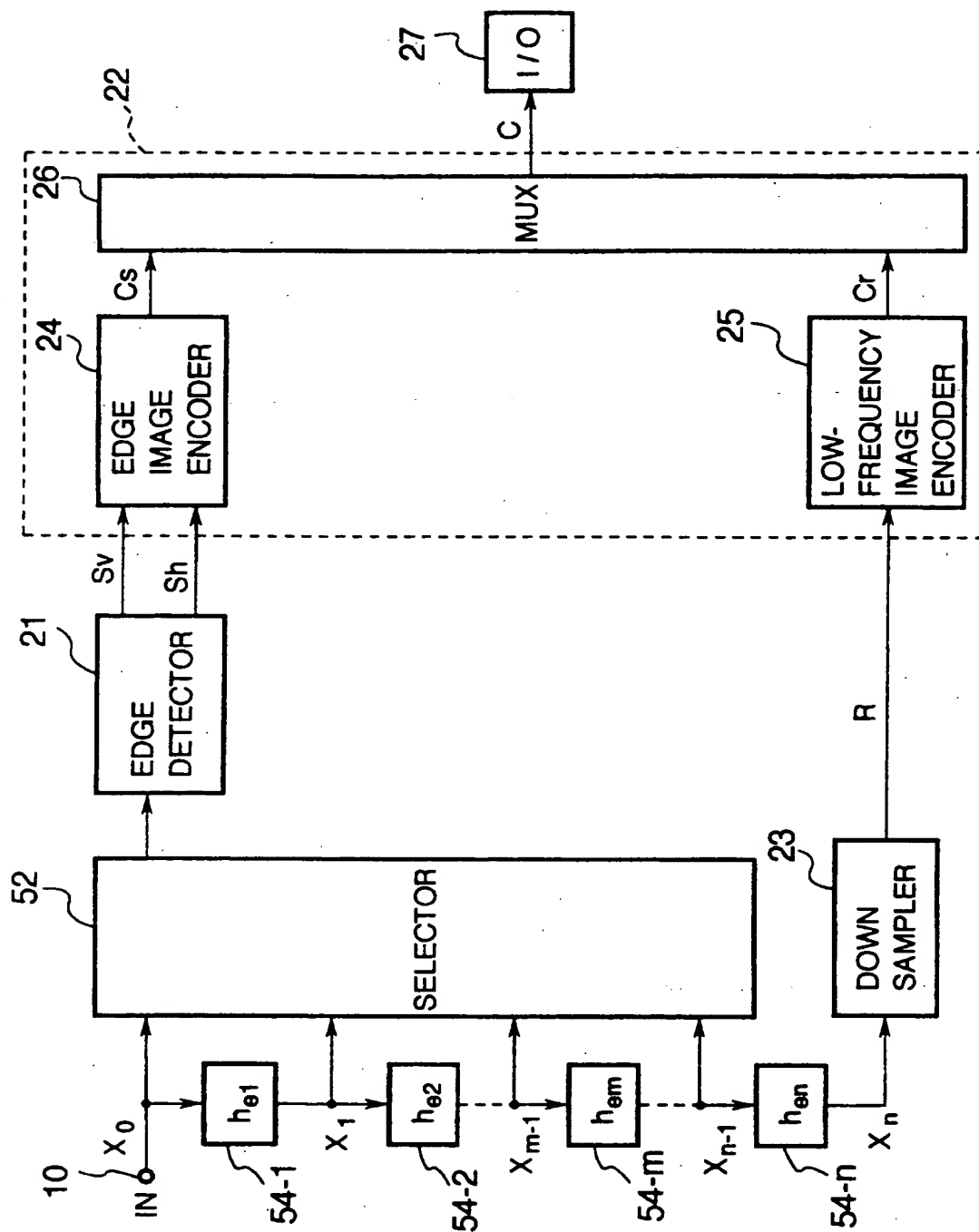


FIG. 13

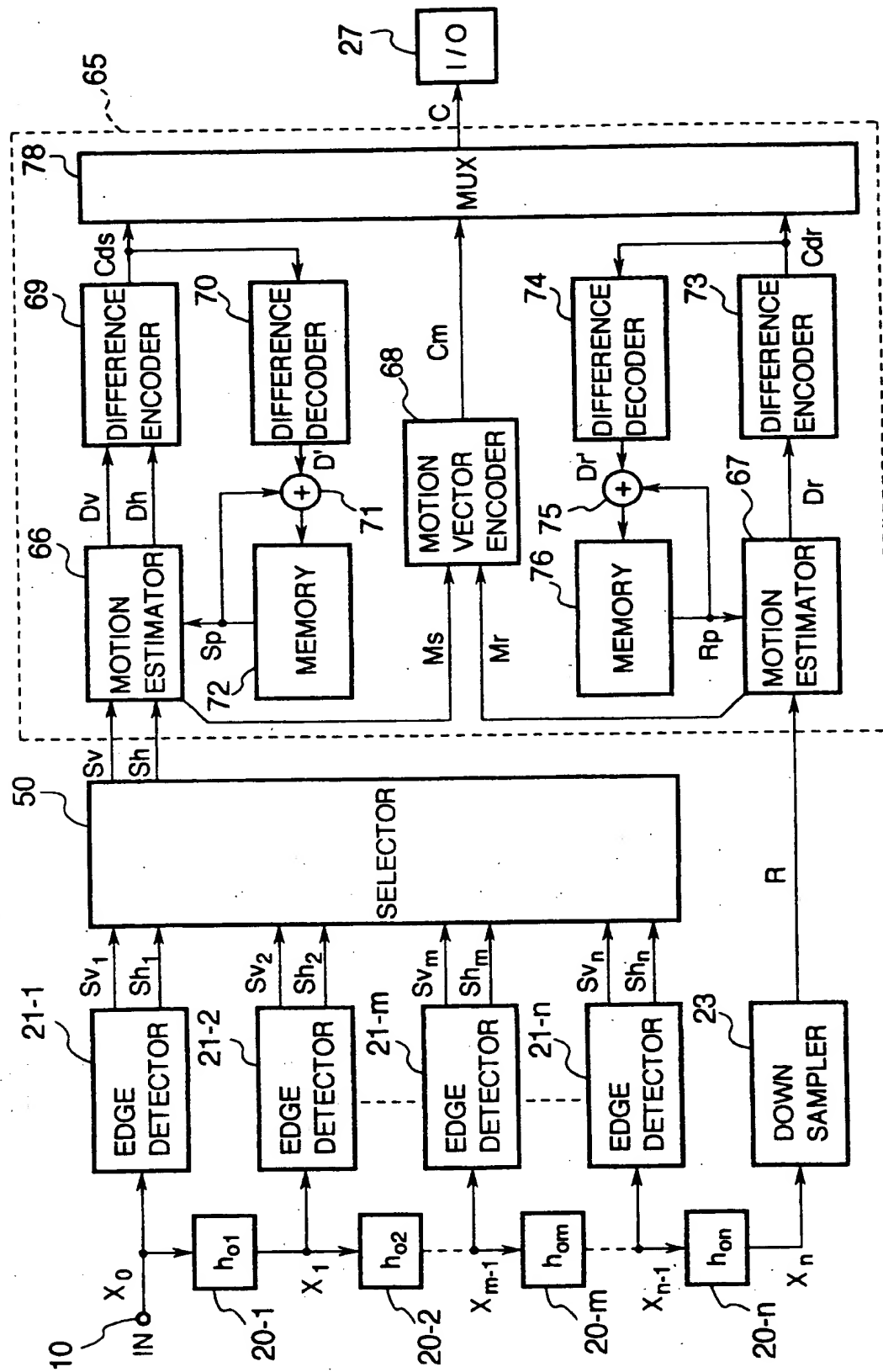


FIG. 14

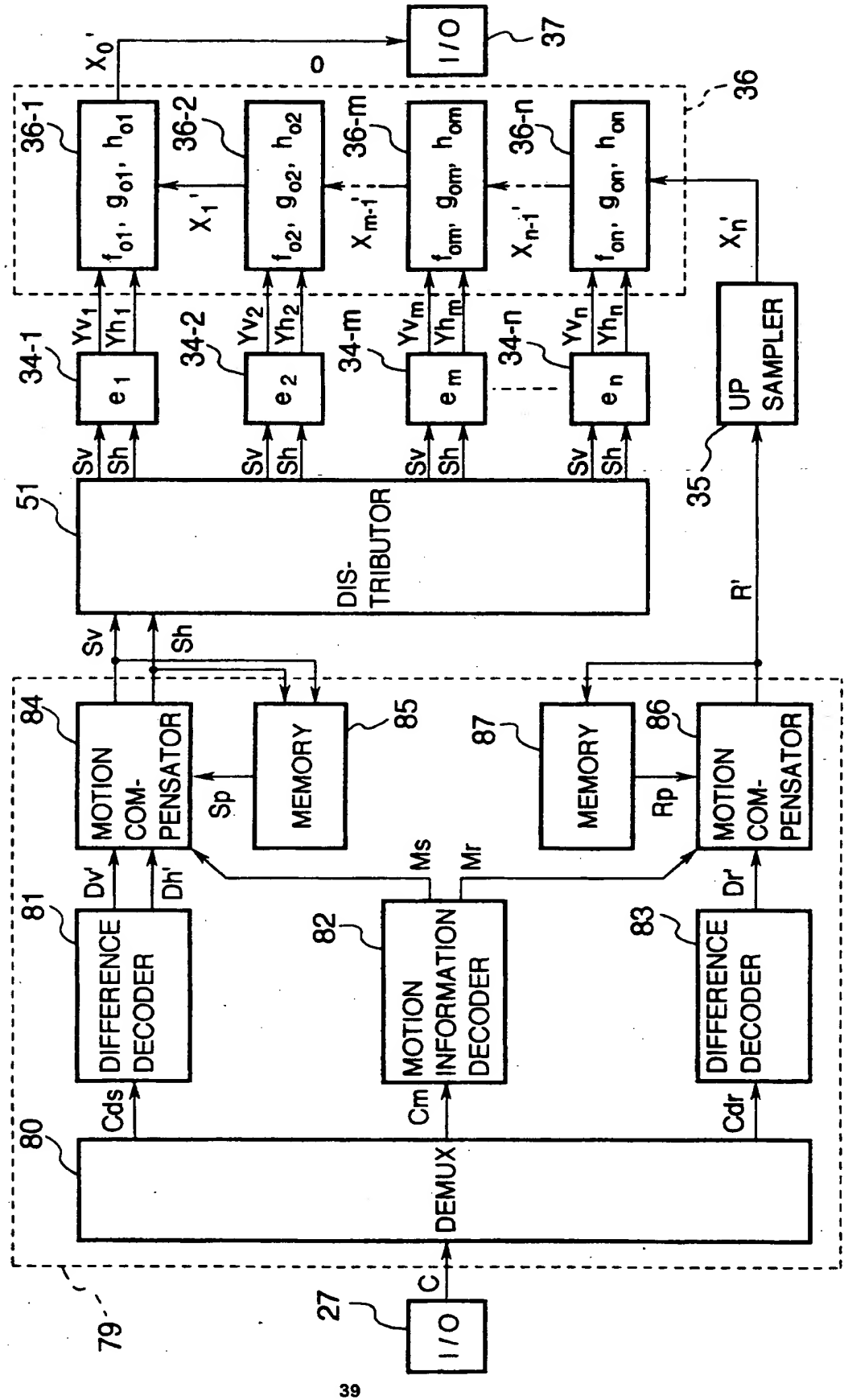


FIG. 15

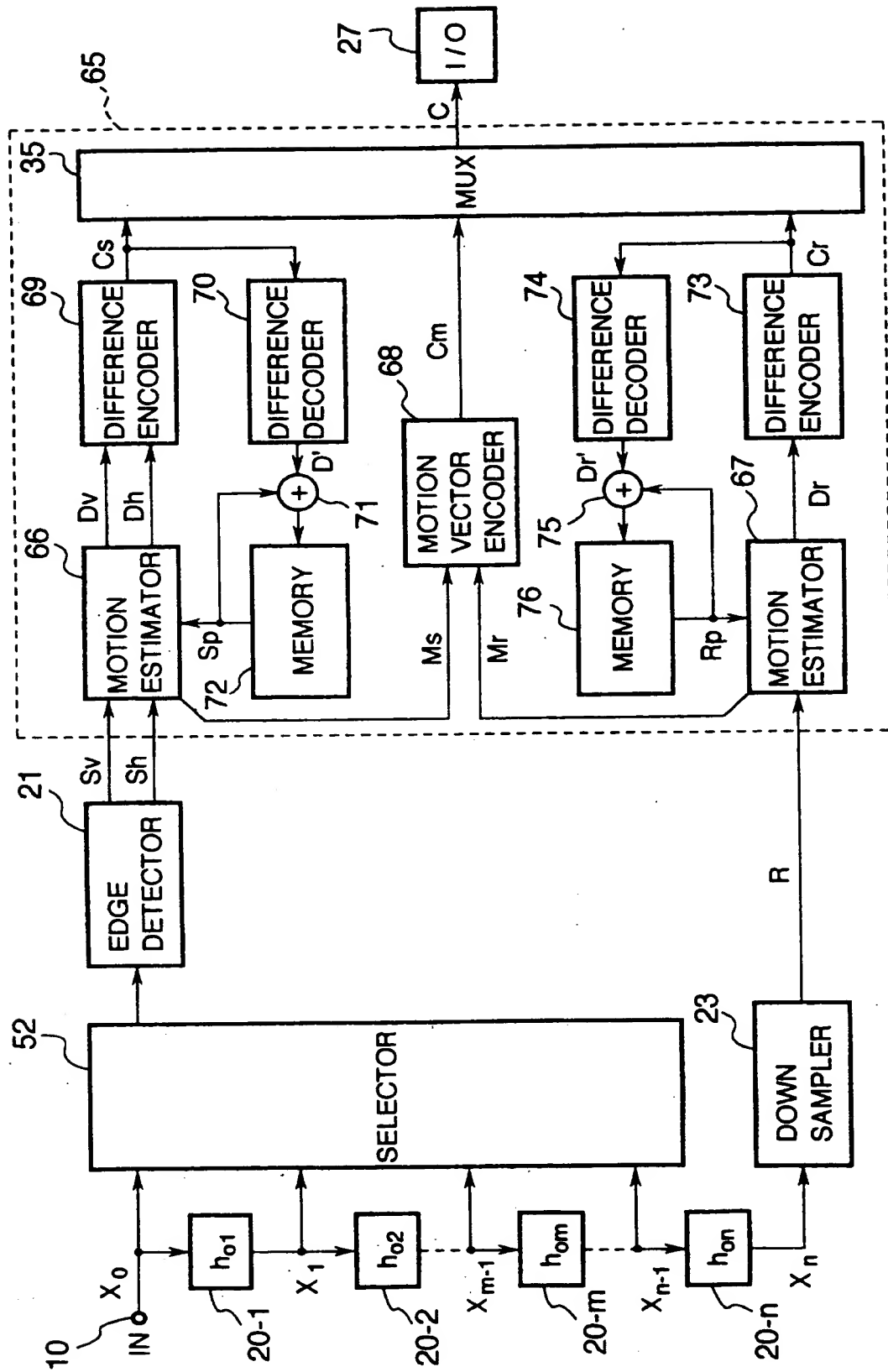


FIG. 16

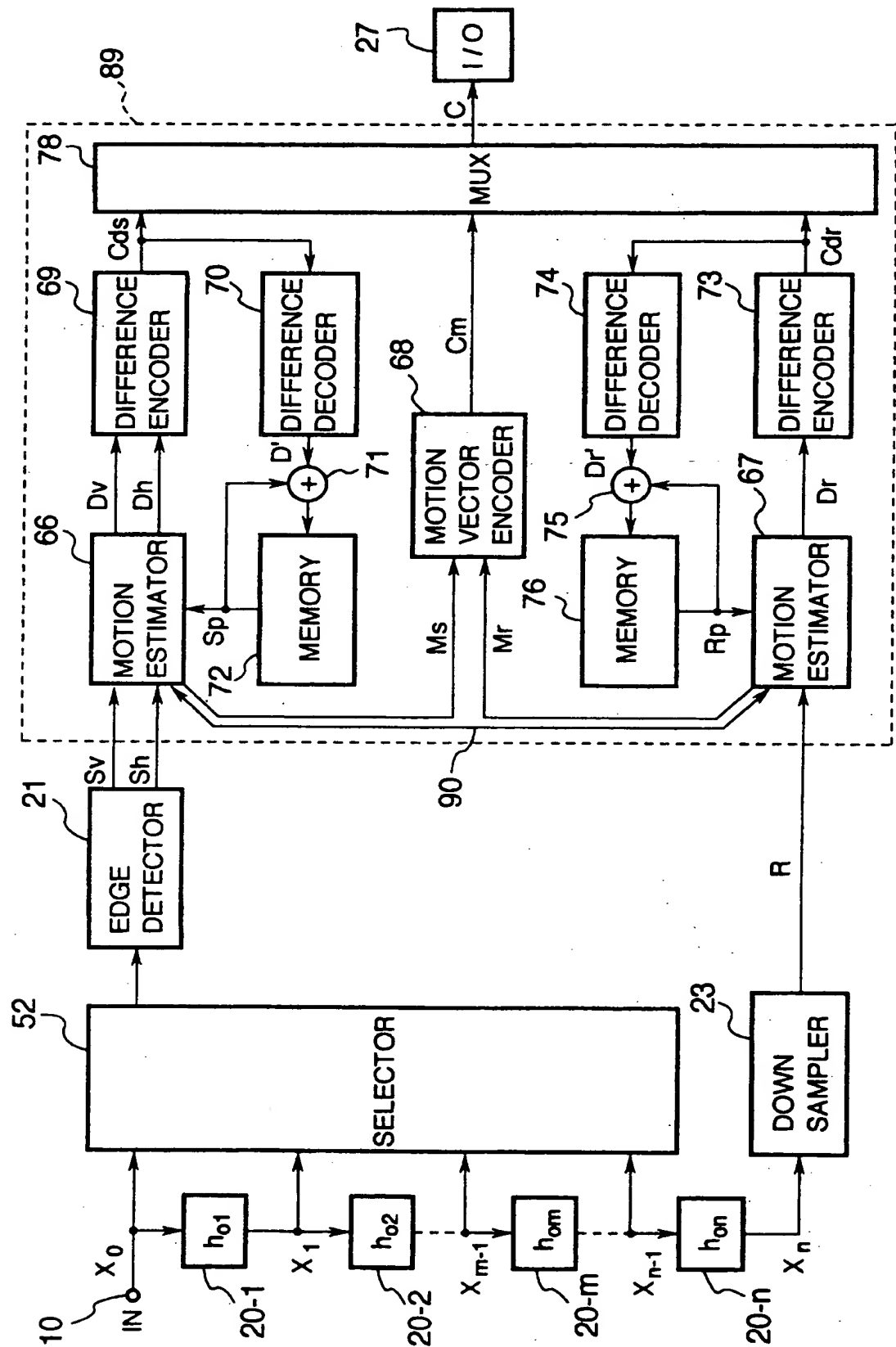


FIG. 17

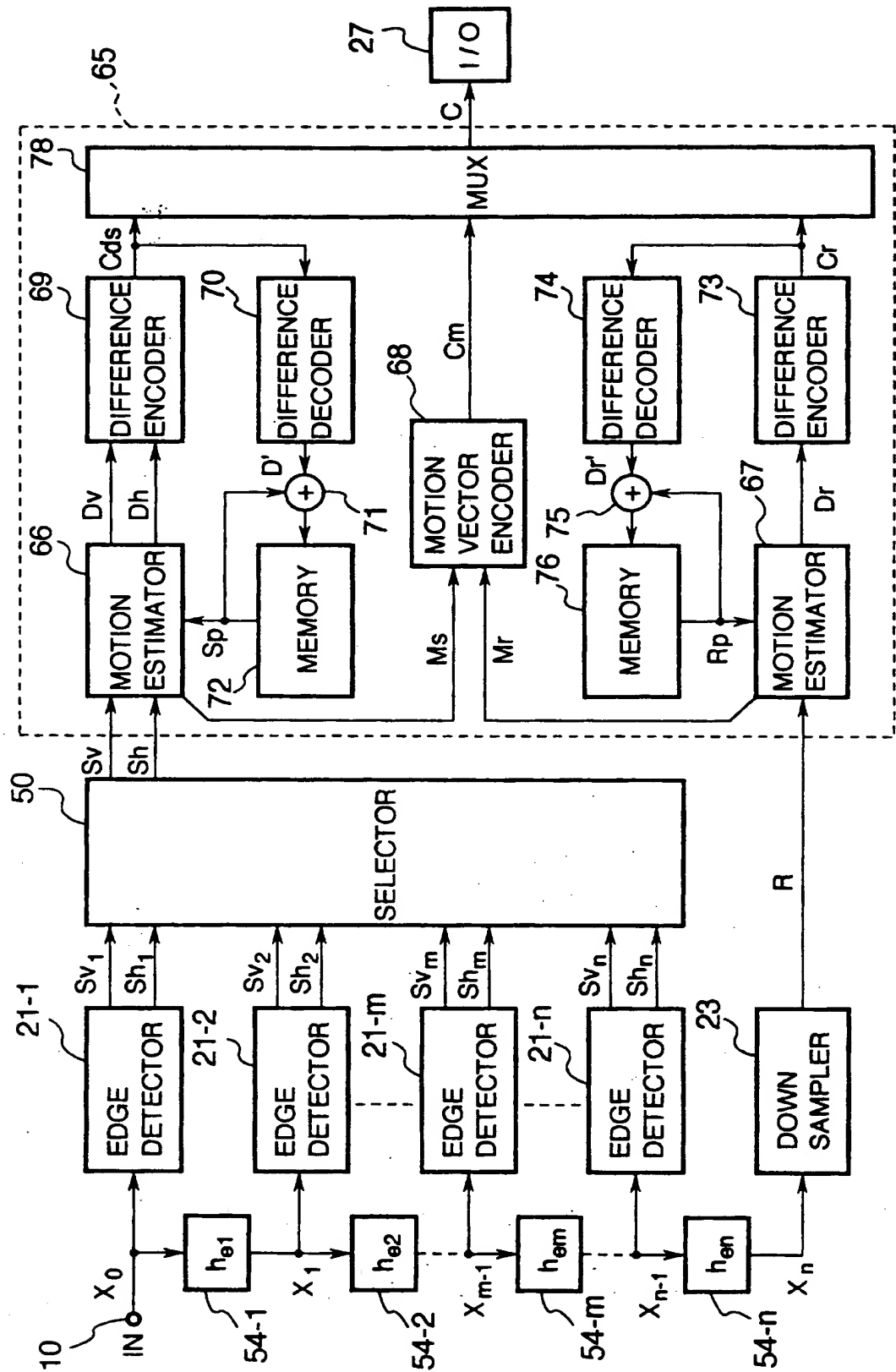


FIG. 18

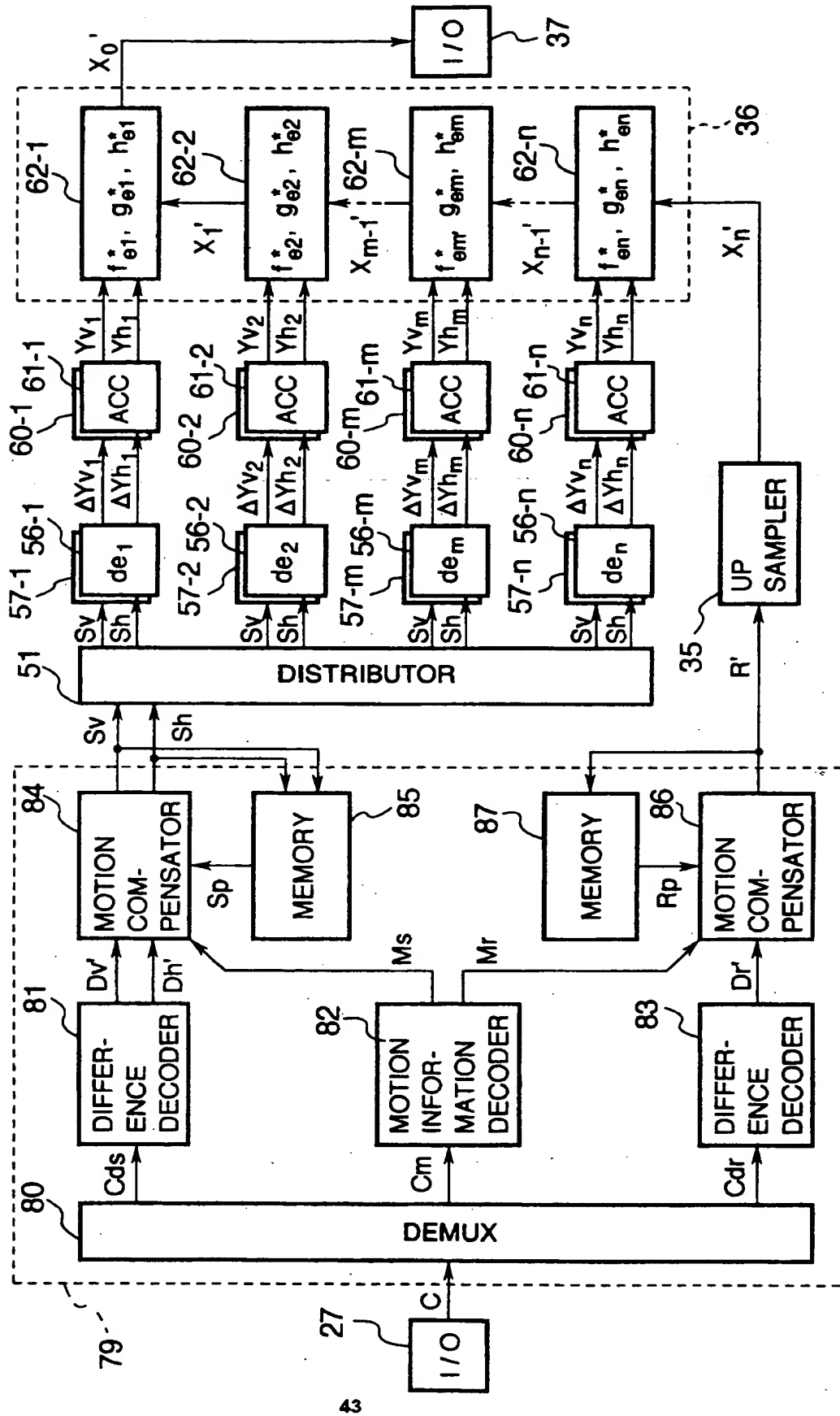


FIG. 19

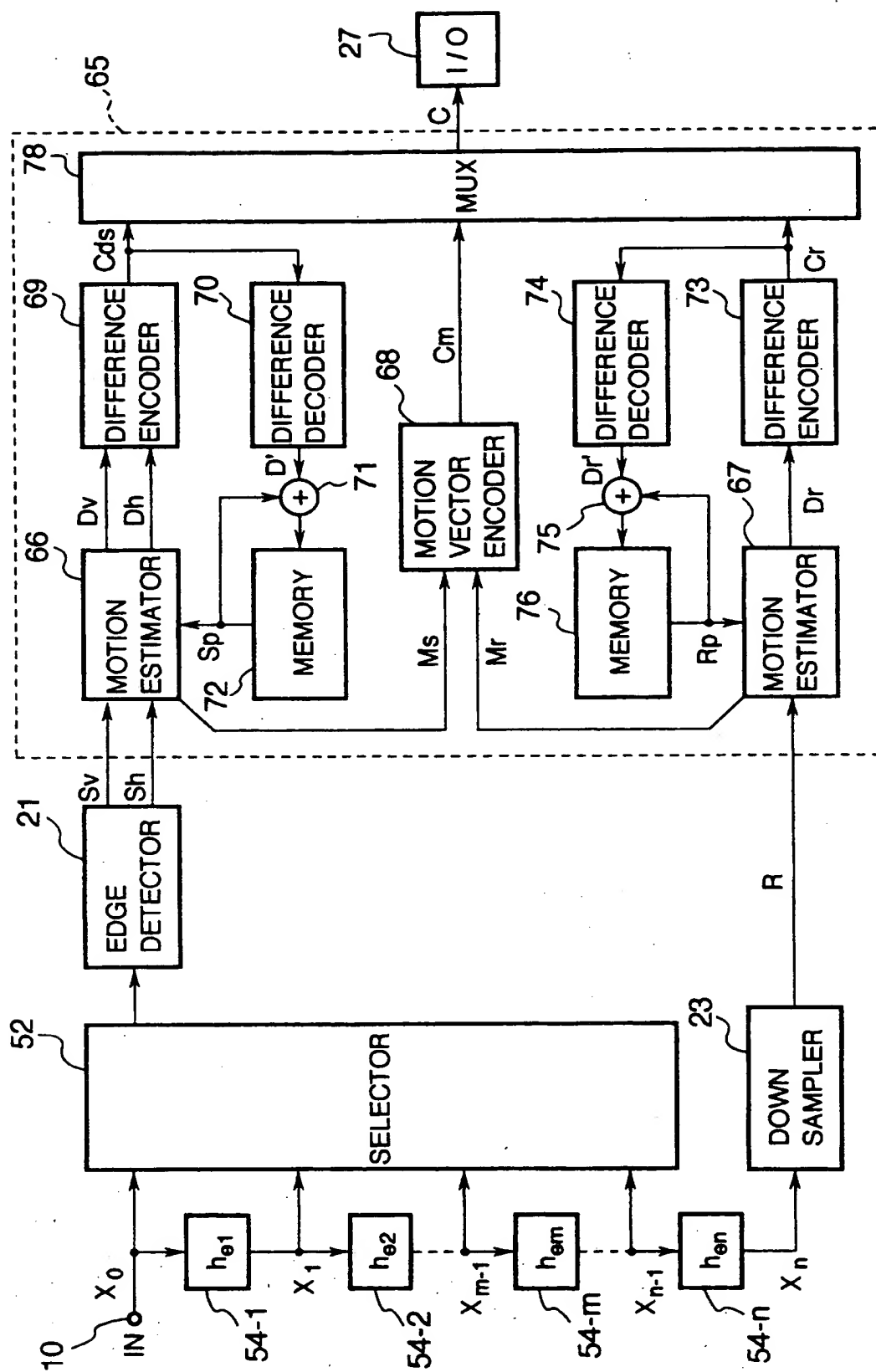
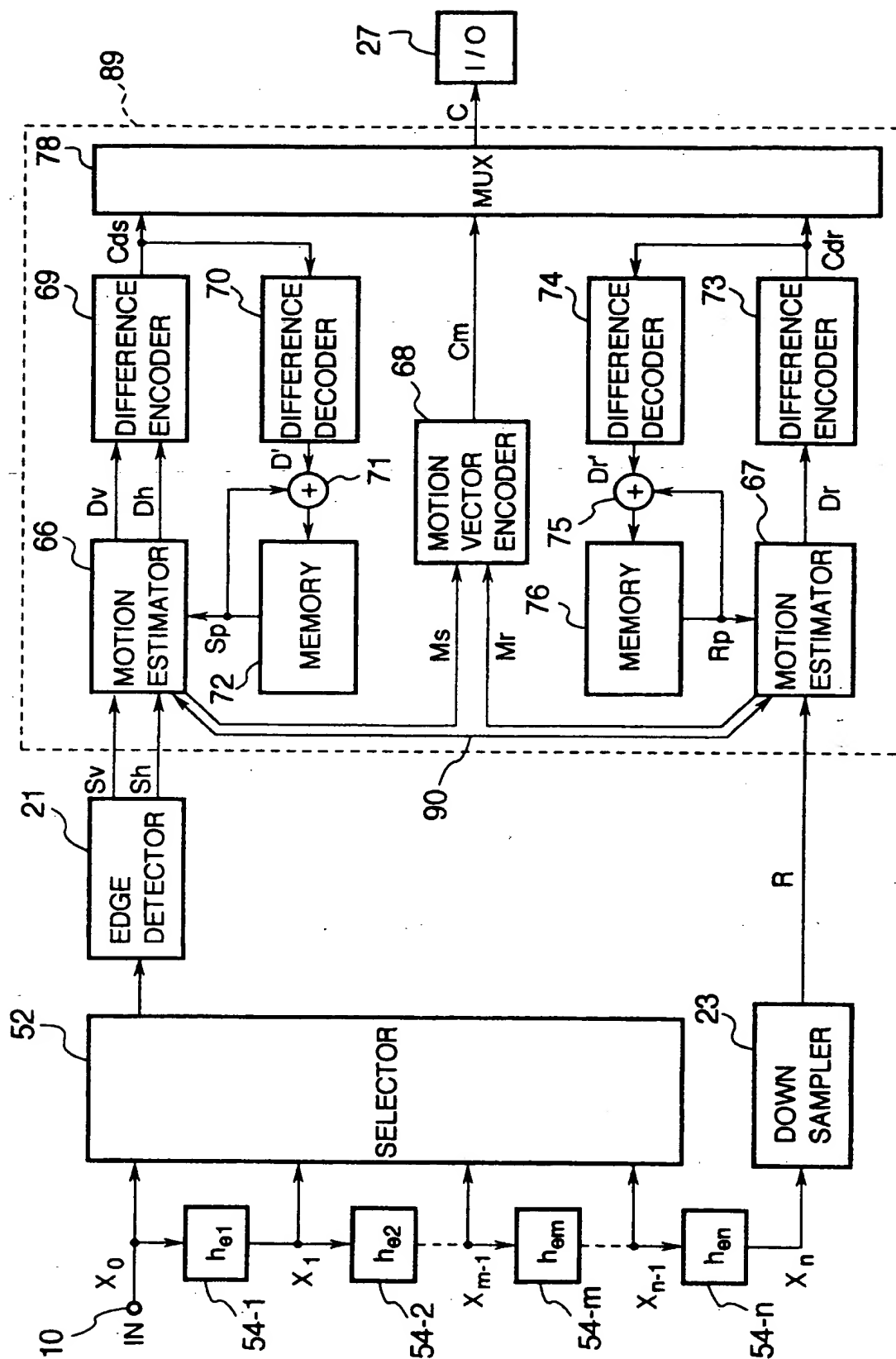


FIG. 20



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(19)



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(11)

**EP 0 679 032 A3**

(12)

**EUROPEAN PATENT APPLICATION**

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Tokyo (JP)

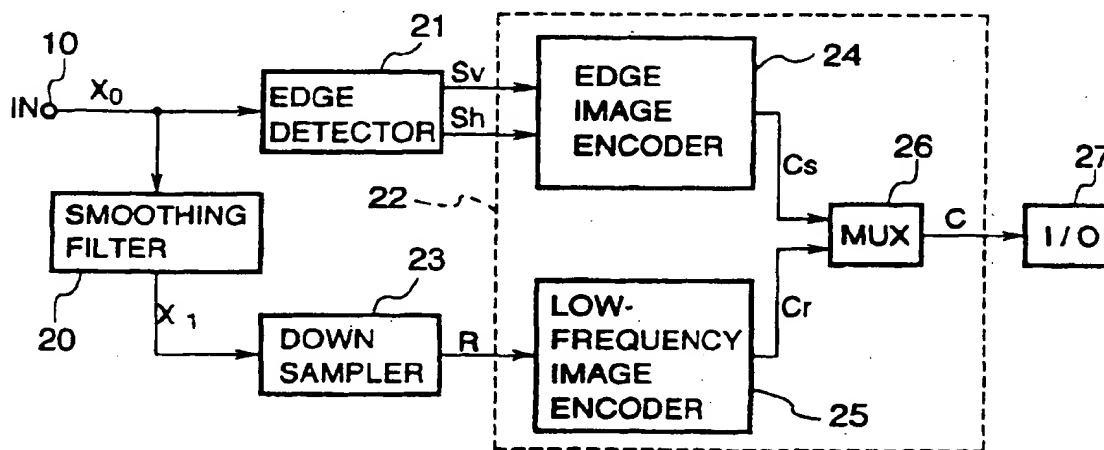
(72) Inventor: Go, Shiyu,  
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20 Little Britain  
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(54) **Image encoding and decoding method and apparatus using edge synthesis and inverse wavelet transform**

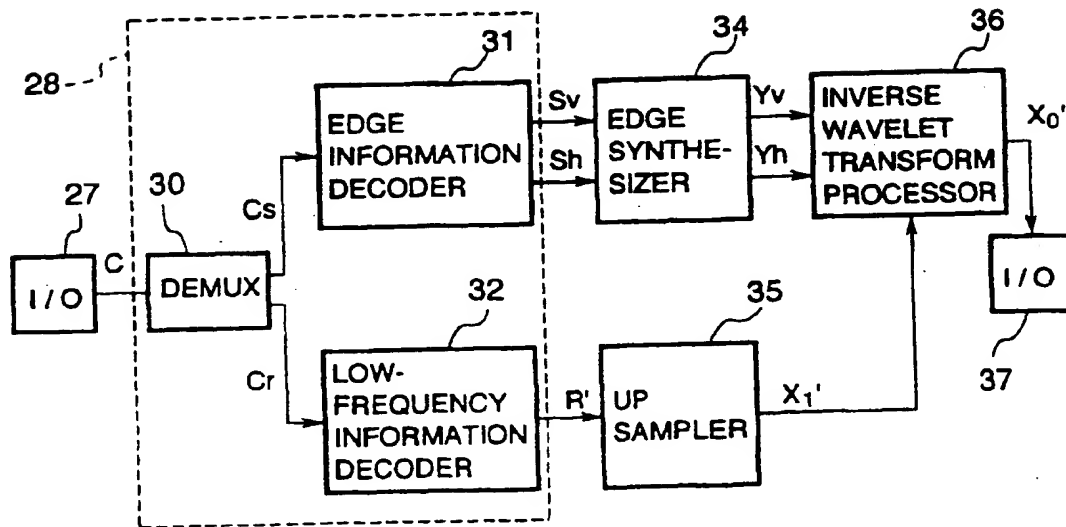
(57) A digitized image is encoded by detecting edges in the image, encoding the position and sharpness of the detected edges, filtering the image by a low-pass filter to generate a low-frequency image, and encoding the low-frequency image. A digitized image encoded in this way is reconstructed by generating a horizontal

edge image and a vertical edge image from the encoded edge position and sharpness information, synthesizing a pair of high-frequency images by filtering the horizontal and vertical edge images with an edge synthesis filter, decoding the low-frequency image, and performing an inverse wavelet transform on the decoded low-frequency image and the high-frequency images.

**FIG. 1**

EP 0 679 032 A3

FIG. 2





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# EUROPEAN SEARCH REPORT

Application Number  
EP 95 30 2620

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.6)
X	IEEE TRANSACTIONS ON PATTERN ANALYSIS AND MACHINE INTELLIGENCE, vol. 14, no. 7, 1 July 1992, pages 710-732, XP000290514 MALLAT S ET AL: "CHARACTERIZATION OF SIGNALS FROM MULTISCALE EDGES"	15-18, 57-60	H04N7/26
A	* paragraph IX *	1-14, 19-56, 61-96	
X	DISCOVERING A NEW WORLD OF COMMUNICATIONS, CHICAGO, JUNE 14 - 18, 1992, vol. 1 OF 4, 14 June 1992, INSTITUTE OF ELECTRICAL AND ELECTRONICS ENGINEERS, pages 532-536, XP000326919 YU P ET AL: "EDGE MODIFIED VECTOR QUANTIZATION FOR IMAGE COMPRESSION"	15-18, 57-60	
A	* paragraph 2.1.1 *	1,19,20, 36,61,70	
	* paragraph 2.2 - paragraph 2.2.3 *		
A	SIGNAL PROCESSING. IMAGE COMMUNICATION, vol. 2, no. 4, 1 December 1990, pages 429-440, XP000234776 GIUNTA G ET AL: "IMAGE SEQUENCE CODING USING ORIENTED EDGES"	1-96	TECHNICAL FIELDS SEARCHED (Int.Cl.6) H04N
A	WO-A-93 13624 (INTEL CORP) 8 July 1993		
A	WO-A-93 20653 (BELL COMMUNICATIONS RES) 14 October 1993		
	--- -/--		
The present search report has been drawn up for all claims			
Place of search THE HAGUE		Date of completion of the search 20 December 1996	Examiner Poirier, J-M
CATEGORY OF CITED DOCUMENTS X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document			

EPO FORM 1503 (01/92) (P04C01)



European Patent  
Office

# EUROPEAN SEARCH REPORT

Application Number  
EP 95 30 2620

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.6)
A	ELECTRONICS & COMMUNICATIONS IN JAPAN, PART I - COMMUNICATIONS, vol. 77, no. 3, 1 March 1994, pages 25-40, XP000445301 TSUYOSHI HANAMURA ET AL: "HIERARCHICAL VIDEO CODING SCHEME WITH SCALABILITY AND COMPATIBILITY"		
A	INTERNATIONAL CONFERENCE ON IMAGE PROCESSING AND ITS APPLICATIONS, 18 July 1989, pages 536-540, XP000519209 MORAN D ET AL: "REGION AND TEXTURE CODING OF TV PICTURES"		
			TECHNICAL FIELDS SEARCHED (Int.Cl.6)
The present search report has been drawn up for all claims			
Place of search THE HAGUE		Date of completion of the search 20 December 1996	Examiner Poirier, J-M
<p><b>CATEGORY OF CITED DOCUMENTS</b></p> <p>X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document</p> <p>T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons &amp; : member of the same patent family, corresponding document</p>			

EPO FORM 150 (12/1996) (P6/CV)